



VALLURUPALLI NAGESWARA RAO VIGNANA JYOTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

An Autonomous, ISO 9001:2015 & QS I-Gauge Diamond Rated Institute, Accredited by NAAC with 'A++' Grade
NBA Accreditation for CE, EEE, ME, ECE, CSE, EIE, IT B.Tech. Programmes
Approved by AICTE, New Delhi, Affiliated to JNTUH, NIRF(2019) 109 Rank in engineering Category
Recognized as "College with Potential for Excellence" by UGC
VignanaJyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad – 500 090, TS, India.
Telephone No: 040-2304 2758/59/60, Fax: 040-23042761
E-mail: postbox@vnrvjiet.ac.in, Website: www.vnrvjiet.ac.in



M.TECH. (POWER ELECTRONICS)

PROGRAM EDUCATIONAL OBJECTIVES (PEOs)

PEO-I: Proficient in applying sustainable and inclusive technologies to analyse, formulate and provide solutions for real time problems in diversified fields.

PEO-II: Solve complex technological problems using emerging technologies and tools

PEO-III: Work effectively as an individual and team member with good communication skills in project execution.

PEO-IV: Demonstrate interdisciplinary skills and professional ethics in relating engineering issues to broader societal context.

PEO-V: Engage in life long learning for a successful professional career.



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PROGRAM OUTCOMES (POs)

- PO1:** An ability to independently carry out research /investigation and development work to solve practical problems.
- PO2:** An ability to write and present a substantial technical report/document.
- PO3:** Students should be able to demonstrate a degree of mastery over the area as per the specialization of the program. The mastery should be at a level higher than the requirements in the appropriate bachelor program.
- PO4:** Design and conduct experiments, as well as analyze the power electronic converters & drives and interpret the data.
- PO5:** Function on multidisciplinary technological issues assimilating power electronics advancements.
- PO6:** Use the techniques, skills, and modern engineering simulation tools necessary for the design and development of power converter topologies and engage in life long learning.