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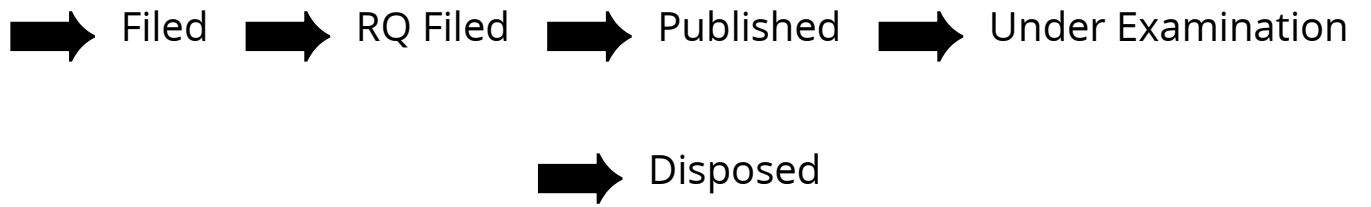


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(51) International classification	:H03K0007080000, H02M0007483000, H02M0007527000, H02P0029024000, H02P0027080000	(71) <b>Name of Applicant :</b> <b>1)MR. SADANANDAM PERUMANDLA</b> Address of Applicant :EEE Department, Vaagdevi College of Engineering Khammam - Warangal Hwy Road, Bollikunta , Warangal Telangana India
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(57) Abstract :

A circuit 100 method for operating OEWIM 106 using a dual inverter 104 characterized in that the OEWIM 106 is fed with a modulated frequency triangular carrier based discontinues pulse width modulation (DPWM) using digital signal processor (DSP) controller 108 is disclosed. The circuit 100 includes a battery or rectifier 102, a dual inverter 104, an OEWIM 106, and a DSP controller 108. The dual inverter 104 receives at least one of a lower Direct Current (DC) link voltage and a higher DC link voltage from the battery 102. The dual inventor 104 receives the modulate frequency based DPWM by comparing the triangular carrier signal with modulated signal. The comparison is performed by the DSP controller 108. The DSP controller 108 is configured to operate the dual inventor 104 in at least one of a symmetrical mode and an asymmetrical mode using the modulated frequency based DPWM and DC link voltages. FIG. 1

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