

Office of the Controller General of Patents, Designs & Trade Marks Department of Industrial Policy & Promotion, Ministry of Commerce & Industry, Government of India

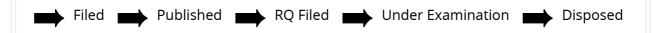
# (http://ipindia.nic.in/index.htm)



	GEOGRAPHICAL INDICATIONS	
Application Details		
APPLICATION NUMBER	202241027437	
APPLICATION TYPE	ORDINARY APPLICATION	
DATE OF FILING	12/05/2022	
APPLICANT NAME	VALLURUPALLI NAGESWARA RAO VIGNANA JYOTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY (VNRVJIET)	
TITLE OF INVENTION	A Series Capacitive Compensation Technique with Design Based Iterative Algorithm for Mitigation of Ferranti Effect in EHV and UHV Power Transmission Systems	
FIELD OF INVENTION	ELECTRICAL	
E-MAIL (As Per Record)	ravirlyfan@gmail.com	
ADDITIONAL-EMAIL (As Per Record)		
E-MAIL (UPDATED Online)		
PRIORITY DATE		
REQUEST FOR EXAMINATION DATE		
PUBLICATION DATE (U/S 11A)	27/05/2022	

Application Status	
APPLICATION STATUS	Awaiting Request for Examination

View Documents



In case of any discrepancy in status, kindly contact ipo-helpdesk@nic.in

(12) PATENT APPLICATION PUBLICATION

(19) INDIA

(51) International

(86) International

(87) International

Publication No

Filing Date

Filing Date

**Application Number** 

Filing Date

(62) Divisional to

(61) Patent of Addition :NA

to Application Number :NA

Application No

classification

(22) Date of filing of Application: 12/05/2022

(21) Application No.202241027437 A

(43) Publication Date: 27/05/2022

(54) Title of the invention: A Series Capacitive Compensation Technique with Design Based Iterative Algorithm for Mitigation of Ferranti Effect in EHV and UHV Power Transmission Systems

:H02H0009040000, H04L0012280000,

H02J0003240000, C02F0001680000,

H02J0050800000

:PCT//

: NA

:NA

:NA

:01/01/1900

(71)Name of Applicant:

# 1)VALLURUPALLI NAGESWARA RAO VIGNANA JYOTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY (VNRVJIET)

Address of Applicant: VNRVJIET VignanaJyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad- 500090, Telangana State, India Nizampet ------

Name of Applicant : NA Address of Applicant : NA (72)Name of Inventor :

#### 1)Dr. VENU YARLAGADDA

Address of Applicant :EEED, VNRVJIET Vignana Jyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad- 500090, Telangana State, India Hyderabad ------

#### 2)Ms. ANNAPURNA KARTHIKA GARIKAPATI

Address of Applicant :EEED, VNRVJIET Vignana Jyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad- 500090, Telangana State, India Hyderabad ------

#### 3)Mr. B.DEVULAL

Address of Applicant :EEED, VNRVJIET Vignana Jyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad- 500090, Telangana State, India Hyderabad ------

# 4)Mr. ANJAN BOORUGU

Address of Applicant :EEED, VNRVJIET Vignana Jyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad- 500090, Telangana State, India Hyderabad ------

## 5)Dr. CHAVA SUNIL KUMAR

Address of Applicant :EEED,BVRIET College of Engineering for Women, Rajiv Gandhi Nagar, Hyderabad- 500090, Telangana State, India Hyderabad ------

# 6)Dr. A. GANGA DINESH KUMAR

Address of Applicant :EEED, Malla reddy Engineering College for Women , Maisammaguda, Dhulapally,(P.O) Secundrabad Hyderabad - 500100, Telangana State, India Hyderabad ------

7)Dr. KOVELAMUDI SHARMILA RUDRAMAMBA

Address of Applicant: H&SD, VNR VJIET Vignana Jyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad- 500090, Telangana State, India Hyderabad ------

# (57) Abstract:

Currently all FACTS devices are aiming at improvement in power transmission system performance. A simple, robust and flexible device has been invented for further in continuation of research in power sector. Current invention related to series capacitive compensation, which is novel technique has been discovered. This simple device is used to get rid of all complexity in control of various FACTS devices can be avoided and can be effectively implement it in practical systems and is most use full in all no load and loading conditions of Electric Transmission Systems. The novel invention related to implementation of series capacitive compensation, which is novel technique has discovered in order to avoid the complexity of FACTS devices and their control. This simple device is used to get rid of all complexity in control of various FACTS devices can be avoided and can be effectively implement it in practical systems.

No. of Pages: 11 No. of Claims: 3