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APPLICATION TYPE	ORDINARY APPLICATION
DATE OF FILING	12/05/2022
APPLICANT NAME	VALLURUPALLI NAGESWARA RAO VIGNANA JYOTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY (VNRVJIET)
TITLE OF INVENTION	A Series Capacitive Compensation Technique with Design Based Iterative Algorithm for Mitigation of Ferranti Effect in EHV and UHV Power Transmission Systems
FIELD OF INVENTION	ELECTRICAL
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Application Status

APPLICATION STATUS	Awaiting Request for Examination
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<p>(51) International classification :H02H0009040000, H04L0012280000, H02J0003240000, C02F0001680000, H02J0050800000</p> <p>(86) International Application No Filing Date :PCT// :01/01/1900</p> <p>(87) International Publication No : NA</p> <p>(61) Patent of Addition to Application Number Filing Date :NA :NA</p> <p>(62) Divisional to Application Number Filing Date :NA :NA</p>	<p>(71)Name of Applicant : 1)VALLURUPALLI NAGESWARA RAO VIGNANA JYOTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY (VNRVJIET) Address of Applicant :VNRVJIET VignanaJyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad- 500090, Telangana State, India Nizampet -----</p> <p>Name of Applicant : NA Address of Applicant : NA</p> <p>(72)Name of Inventor : 1)Dr. VENU YARLAGADDA Address of Applicant :EEED,VNRVJIET VignanaJyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad- 500090, Telangana State, India Hyderabad -----</p> <p>2)Ms. ANNAPURNA KARTHIKA GARIKAPATI Address of Applicant :EEED,VNRVJIET VignanaJyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad- 500090, Telangana State, India Hyderabad -----</p> <p>3)Mr. B.DEVULAL Address of Applicant :EEED,VNRVJIET VignanaJyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad- 500090, Telangana State, India Hyderabad -----</p> <p>4)Mr. ANJAN BOORUGU Address of Applicant :EEED,VNRVJIET VignanaJyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad- 500090, Telangana State, India Hyderabad -----</p> <p>5)Dr. CHAVA SUNIL KUMAR Address of Applicant :EEED,BVRIET College of Engineering for Women, Rajiv Gandhi Nagar, Hyderabad- 500090, Telangana State, India Hyderabad -----</p> <p>6)Dr. A. GANGA DINESH KUMAR Address of Applicant :EEED, Malla reddy Engineering College for Women , Maisammaguda, Dhulapally,(P.O) Secunrabad Hyderabad- 500100, Telangana State, India Hyderabad -----</p> <p>7)Dr. KOVELAMUDI SHARMILA RUDRAMAMBA Address of Applicant :H&SD,VNRVJIET VignanaJyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad- 500090, Telangana State, India Hyderabad -----</p>
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(57) Abstract :
Currently all FACTS devices are aiming at improvement in power transmission system performance. A simple, robust and flexible device has been invented for further in continuation of research in power sector. Current invention related to series capacitive compensation, which is novel technique has been discovered. This simple device is used to get rid of all complexity in control of various FACTS devices can be avoided and can be effectively implement it in practical systems and is most use full in all no load and loading conditions of Electric Transmission Systems. The novel invention related to implementation of series capacitive compensation, which is novel technique has discovered in order to avoid the complexity of FACTS devices and their control. This simple device is used to get rid of all complexity in control of various FACTS devices can be avoided and can be effectively implement it in practical systems.

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