

Name: P V Gopikumar

Designation: Asst. Professor

Department: EIE

Mail Id: gopikumar_pv@vnrvjiet.in



Experience (in years): 12 Teaching: 11 Research: Others if any Specify: 1

1 Educational / Technical qualifications:

S.No	Level (UG / PG / Ph.D)	Year of passing	Specialization
1	M.Tech	2013	DSCE
2	B.Tech	2006	EIE
3	Inter	2001	MPC
4	S.S.C	1999	

2 Teaching and Learning:

2.1 Teaching Interests: MPMC,SSC,S&S,PDC,CS,DSP,ECA,FPGA & CPLD, ERTS,CO,IPI,NNFL,FOLI and ES.

2.2 Novel Teaching & Learning Techniques adopted: POGIL, WIT & WIL, Story Board

2.3 Involvement in curriculum updating / Design:

3 3. Co-curricular and Extra-Curricular Activities

3.1 Professional Society Memberhips:-

3.2 Interests and Hobbies: gardening, cooking and circuit designing

3.3 CCA/ECA Organized:

3.4 CCA/ECA participated:

3.5 Counseling and Mentoring Activity: Involved in Mentoring of Eight students per class each year

3.6 Committees involved in:

Department level: Training and Placement coordinator, CDC coordinator & EDC coordinator

Institute Level: PA systems

4 Conference / Workshop / Seminar / Guest Lectures :

4.1 Conducted:

4.2 Attended:

Attended to two day seminar on Recent Trends in Laser Technology & its Applications

Registered online course on Python 3 programming beginners to pro masterclass

Attended webinar on Need of the hour wake up find solutions (Save People and Save Country)

Attended webinar on Virtual Lab & Their Importance

Attended Webinars on Factory Automation using Siemens TIA Portal and Introduction to Patent writing and Associated Protocol

Participated in Biotechnology E-Quiz Programme

Completed the online Quiz on "Intellectual Property Rights"

Participated in 5 days Virtual faculty development programme

Registered online course on Machine Learning using Python Online project based course

Attended to one week FDP on "Effective online Teaching using ICT Tools "

Attended to FDP on Control Theory and Applications by VITS in association with TASK.

5 Academic Contribution and Research & Consultancy:

5.1 Invited Lectures:

5.2 Articles /Chapters published in Books:

5.3 Books published as single author or as editor:

5.4 Projects Guided :

a) UG : 11 b) PG :

5.5 Research Interests : VLSI & Embedded Systems

5.6 Ph.D students :

a) Enrolled :nil b) Submitted : nil c) Awarded :nil

5.7 Papers published in reviewed journals :

S.No	Title of the Paper	Journal Name Vol.No. PP	ISBN/ISSN No.	Impact Factor/ Citation Index	National/ International
1	Fault detection by using random and deterministic test pattern techniques in a mixed mode BIST environment	Vol 2, issue 5	2248-9622	1.6	international
2	C Bounded Model Checker to Detect unspecified Expression in FreeRTOS	Volume 7	2277-3916		national
3	Non Invsive blood flow measurement using PPG	Volume 6, issue 1	2320-2882	5.97	international
4	Novel Sierpinski Carpet Fractal Antenna for Multiband Wireless Applications	Volume 6, issue 1	2321-9653	6.887	international
5	Home security control system using Raspberry Pi	Volume 4, issue 6			International
6	An Energy and efficient IoT Architecture for Bio Medical Applications	Volume 14, issue 3	0022-1945		International
7	QRS complex Detection and Monitoring Heart Abnormalities in ECG	Volume 17, issue 4	0973-4562		International

5.8: Papers published in reviewed conferences

5.9. Sponsored research Projects:

S.No	Title	Agency	Period	Grant amount	Ongoing / Completed
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5.10 Consultancy Projects:

S.No	Title	Agency	Period	Sanctioned Amount	Ongoing / Completed

6 Awards / Honors received:

2010 GATE qualified, registered for Ph.D in annamalai University

7 **Motto:**

Want to do research work on field of Embedded systems and VLSI technology