

Name: P V Gopikumar

Designation: Asst. Professor

Department: EIE

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Experience (in years): 7 Teaching: 6 Research: 1 Others(if any, specify):

1. Educational / Technical qualifications:

S.No	Level (UG / PG / Ph.D)	Year of passing	Specialization
1	M.Tech	2013	DSCE
2	B.Tech	2006	EIE
3	Inter	2001	MPC
4	S.S.C	1999	

2. Teaching and Learning:

2.1 Teaching Interests: MPMC, SSC, S&S, PDC, CS, DSP, ECA

2.1. Novel Teaching & Learning Techniques adopted: POGIL, WIT & WIL

2.2. Involvement in curriculum updating / Design:

3. Co-curricular and Extra-Curricular Activities

3.1. Interests and Hobbies: gardening, cooking and circuit designing

3.2. CCA/ECA Organized:

3.3. CCA/ECA participated:

3.4. Counseling and Mentoring Activity: Involved in Mentoring of Eight students per class each year

3.5. Committees involved in:

Department level: NBA

Institute Level: PA systems

4. Conference / Workshop / Seminar / Guest Lectures :

4.1 Conducted:

4.2. Attended: Attended to three days work shop on research methodologies

5. Academic Contribution and Research & Consultancy:

5.1. Invited Lectures:

5.2. Articles / Chapters published in Books:

5.3. Books published as single author or as editor:

5.4. Projects Guided :

a) UG : 08 b) PG :

5.5. Research Interests : VLSI & Embedded Systems

5.6. Ph.D students :

a) Enrolled : nil

b) Submitted : nil

c) Awarded : nil

5.7. Papers published in reviewed journals :

S.No	Title of the Paper	Journal Name Vol.No. PP	ISBN/ISSN No.	Impact Factor/ Citation Index	National/ International
1	Fault detection by using random and deterministic test pattern techniques in a mixed mode BIST environment	Vol 2, issue 5	2248-9622	1.6	international

2	C Bounded Model Checker to Detect unspecified Expression in FreeRTOS	Volume 7	2277-3916		national
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5.8. Papers presented at National / International Journals :

S.No	Title of the Paper	Names of the Conference/ Seminars	National/ International	Period
1				

5.9. Sponsored research Projects:

S.No	Title	Agency	Period	Grant amount	Ongoing / Completed

5.10 Consultancy Projects:

S.No	Title	Agency	Period	Sanctioned Amount	Ongoing / Completed

6. Awards / Honors received: 2010 GATE qualified, cleared the Ph.D entrance and attended interview in JNTUH

7. Motto: want to do research work on field of VLSI technology