

VALLURUPALLI NAGESWARA RAO VIGNANA JYOTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

An Autonomous Institute, NAAC Accredited with 'A ++ Grade
NBA Accredited for CE, EEE, ME, ECE, CSE, EIE, IT B.Tech Courses
Approved by AICTE, New Delhi, Affiliated to JNTUH
Recognized as "College with Potential for Excellence" by LIGC

Approved by AICTE, New Delhi, Affiliated to JNTUH
Recognized as "College with Potential for Excellence" by UGC
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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

S.No.	Title of the Project	Funding Agency	Investigators (1. Details of Principal Investigators, 2. Details of Co-Investigators)	Project Duration & Period	Sanctioned Amount (Lakhs)
1	Development of AI Based, efficient and storage independent PV water pump for Agricultural	AICTE (AQIS-2021- 2022) RPS-general	Dr. K. Anuradha, Dr Shiv Prasad Edara, Mrs S. Poornima	3 Years 2022-2025	11.99 Lakhs
2	Applications * Development of hierarchical structures for solar desalination	DST	Dr. K. Ajay Kumar and Dr. P. Naresh	3 Years 2020-2023	40.7
3.	Development of Low Cost-Efficient Charging Station for Electric Vehicle (EV) Charging Applications	TIASN, SEED, DST	Dr.Pasula Naresh Mr. D.S.G. Krishna	3 Years 2019-2022	8.77
4	Deep Learning based Smart Assistant for Blind people	TEQIP-III, JNTUH	Dr. Rashmi Kapoor, Dr. M. Sushama and Dr.M.Aruna Bharathi	2 Years 2019-2020	2.7



5	Data Acquisition Based control design of regular and special electrical machines under MODROBS	AICTE	Dr. K. Anuradha and Mr. G.C. Prabhakar	2 Years 2018-2020	12
6.	National Level Training program (Deep Learning/Machin e Learning/Artificia 1 Intelligence)	DST	Mr. B. Devulal and Dr.Rashmi Kapoor	14 Days	9
7	Development of Advanced Digital Signal Processing & Drives	FIST, DST	Dr. K. Anuradha and Mr. D.S.G. Krishna	5 Years 2015-2020	11.1(50)
8.	Grants under share & Mentor (Margdarshan)	AICTE	Dr. K. Anuradha, Dr.N.Krishna Kumari	3 Years	28
9	International conference on innovations in Electronics, Power, Smart Grids & advanced computing Technologies (IEEPS-2017)	AICTE	Dr.D.Ravi Kumar Dr. K. Anuradha and Dr. J. Viswanatha Rao	2019-22	5 Lakhs
11	Design & Development of Maximum power tracking DC link & static converter for smart mirco grid application	UGC	Mr.D.S.G.Krishna	2 Years, 2014-15	2.85
12	Development and implementation of DSP B algorithm for the protection of power TR	UGC	Mrs. S. Poornima	2 Years, 2014-15	2.5

13	Hardware implementation of FPGA based controller for multilevel inverter	UGC	Mr.G.Sasi Kumar	2 Years, 2014-15	2.375
14	FPGA Implementation of Field Oriented Control for Permanent Magnet Synchronous Motor	UGC	Dr. N.Krishna Kumari	2 Years, 2014-15	3.29 Lakhs
15	Development of Optimization Techniques for Protective Devices and Distributed Generators Allocation to Optimize Reliability and to Reduce Losses in Electrical Power Distribution System	UGC	Mr. D. Ravi Kumar	2 Years, 2014-15	4.959