



**Name:**SASI KUMAR GEDELA

**Designation:**Associate Professor

**Department:** Electrical and Electronics Engineering

**Mail. ID:** sasikumar\_g@vnrvjiet.in

**Experience (in years): Teaching:** 16years **Research:** Nil **Others (If any, Specify):**Nil

### 1. Educational / Technical qualifications:

S.No	Level (UG / PG / Ph.D)	Year of passing	Specialization
1	B.Tech in VR Siddhartha College of Engineering, Vijayawada	1999	Electrical and Electronics Engineering
2	M.tech in JNTUH College of Engineering, Hyderabad	2005	Power Electronics

### 2. Teaching and Learning:

**2.1. Teaching Interests:** :Electrical Distribution systems, Power Electronics and its Applications (both AC & DC Drives), Control System APPLICATIONS, Control of Electric Drives, Electrical Circuits Analysis and Power System Analysis, Network analysis

**2.2. Novel Teaching & Learning Techniques adopted:** WIT and WIL and POGIL

**2.3. Involvement in curriculum updating / Design:**As a Department Academic Member and BOS Member, participated in autonomous course design

### 3. Co-curricular and Extra-Curricular Activities

**3.1. Interests and Hobbies:**Reading Books, News Papers, watching Cricket

**And** To conduct various activities for students to enhance their knowledge

**3.2. CCA/ECA Organized:**Organized Corona-2K9

**3.3. CCA/ECA participated:**EEE Association Member. Power Converters Lab Incharge

**3.4. Counseling and Mentoring Activity:** Mentoring students for regularity and to improve academic performance

**3.5. Committees involved in:**

**Department level:** DAC, BoS member

**Institute Level:** Dept TEQIP Coordinator

### 4. Conference / Workshop / Seminar / Guest Lectures:

#### 4.1 Conducted:

- 1 conducted guest lecture on circuit Analysis techniques under TEQIP sponsored with QEEE from 17-2-15 to 19-2-15
- 2 Conducted a one week national workshop on “Emerging trends in Power Electronics and Power systems”, organized by Dept of Electrical & Electronics Engg during 16<sup>th</sup> May 2016 to 20<sup>th</sup> May 2016

#### 4.2 Attended:

- 1 ‘Power Electronics and drives in Electric and Hybrid vehicles in 2008’ at VasaviEngg college, Hyderabad.
- 2 AICTE Sponsored three day workshop on “Recent Advances in Control of Power Electronic Drives (RACPED-2009), Jan 2 29<sup>th</sup> -31<sup>st</sup>, Andhra University, Vizag.
- 3 Attended a one day tutorial on “ Smart Distribution Grids for Future – Challenges & Possible Solutions “, by Professor S.S. Venkata, Professor Emeritus & Chair , Iowa State University, USA at VNRVJIET, Hydearbad on July 13, 2009
- 4 Attended a three day Workshop on “PSCAD / EMTDC Applications in Electrical Engineering” at VNRVJIET, Hyderabad during 05-05-2009 to 07-05-2009.
- 5 Attended three day workshop on “Simulation of Power Systems using Mi POWER Software” at VNRVJIET during 25-02-2010 to 27-02-2010.

- 6 Attended Expert Lecture on “Real Time Ratings Applications in Transmission Networks” by Dr.SudhakarCherukupalli, Team Leader – Transmission Cable (Design), British Columbia Hydro Power Corporation, Vancouver, Canada, on July 28<sup>th</sup>, 2010 at JNTU Hyderabad, Jointly organized by VNRVJIET and JNTU Hyderabad
- 7 Attended CII sponsored tutorial on “Development of Massive Wind Power Plants: Integration Issues: Developer, Owner & Operators “Perspective” at CII\_Green Business Centre, Hyderabad, 23<sup>rd</sup> August, 2010.
- 8 Attended three day National Conference on “National Power Systems Conference (NPSC) “, during 15-12-10 to 17-12-10 at Osmania University, Hyderabad
- 9 Attended a two day Workshop on “Modelling and Control of Electrical Machines” at VNRVJIET, Hyderabad during 28-01-2011 to 29-01-2011
- 10 Attended Shadow Engineering Programme at HBL Power Systems, Shameerpet, Hyderabad on 08-06-11 to 11-06-11.
- 11 Attended AICTE Sponsored Twelve days Staff Development Programme (SDP) on “Modern Control Techniques for Power Systems (MCTPS – 2012) “at VNR VJIET, Hyderabad during 18-06-12 to 30-06-12
- 12 Attended one day seminar on “Research Publications and Documentation “conducted by Department of Computer Science and Engineering, VNRVJIET, Bachupally, Hyderabad on 04-05-13 under TEQIP-II.
- 13 Attended three day work shop on “Art of Teaching “at, VNRVJIET, Bachupally, Hyderabad during 06-05-2013 to 08-05-13 under TEQIP-II.
- 14 Attended a work shop on “POGIL (Process Guided Inquiry Learning “by VNRVJIET, Hyderabad During the year 2013
- 15 Participated in MATLAB 3 day’s workshop conducted during 26<sup>th</sup> to 28<sup>th</sup> May, 2014 at VNRVJIET
- 16 International work shop on placement of synchro phasor measurement for SMART GRID during 20-11-14 to 22-11-14 at MVGR, Vizianagaram.
- 17 Attended course on Art of Living from 1-6-15 to 6-6-15 at VNRVJIET
- 18 Attended International Conference on “Aero space Electronics, Electrical, Communication and Instrumentation” from 22-10-2016 to 23-10-2016, at VRSEC Vijayawada
- 19 Attended National Conference on “Recent Trends in Power Systems and Drives-2016”, at CBIT, Hyderabad.

## **5. Academic Contribution and Research & Consultancy:**

### **5.1. Invited Lectures:Nil**

### **5.2. Articles/Chapters published in Books: Nil**

### **5.3. Books published as single author or as editor: Nil**

### **5.4. Projects Guided:**

- Guided few B.Tech projects on “speed control of universal motor by Triac” “Solar Inverter , ‘Design of DC to DC converter’, Load Flow Analysis of Embedded Power System using UPFC Controller, ‘Design of SCR based Time delay relay with alarm system for Coal conveyors’, ‘Micro controller based switching of motor using Mobile Phone’, ‘Micro Processor based control of 3-phase Induction Motor’ , ‘Control of DC motors using PWM Chopper’. PWM based speed control of DC motor using micro controller.
- Guided few M. Tech Projects Performance Analysis Of Multilevel Inverter With Stabilized Voltage Levels And Zero Sequence Compensation and An efficient DC to DC converter configuration by soft switching devices
- Guided project on Design of smps(75v,30amp) Regulator for Base transceiver station

- Guided project on optimal location of Fuel based Distributed Generation in distribution systems.
- Guided the project on Slip power flow using Induction Generator
- Guided the project on optimal location of DG in Radial distribution system for minimization of losses.
- Guided the project on security constrained optimal power flow using benders cut principle.
- Guided the project on optimal location of DG in Radial distribution system for minimization of losses
- Guided the project on Reducing the Fault current and Over voltage in a Distribution system with Distributed Generation units through an Active type SFCL
- Presently Guiding the project on optimal location of DG in Radial distribution system for minimization of losses
- Presently Guiding the project on Reducing the Fault current and Over voltage in a Distribution system with Distributed Generation units through an Active type SFCL

**5.5. Research Interests:** Power systems and power Electronics

**5.6. Ph.D students:**

a) **Enrolled:** Nil

b) **Submitted:** Nil

c) **Awarded:** Nil

**5.7. Papers published in reviewed Journals:**

S.No	Title of the Paper	Journal Name Vol.No. PP	ISBN/ISSN No.	Impact Factor/ Citation Index	National/ International
1	Security Constrained Optimal Power Flow Using Benders Cut Principle	International Journal of control theory and applications Vol.8,No.1, pp.271-283	ISSN 0974-5572		International
2	OVER CURRENT AND OVER VOLTAGE DIMINUTION USING ACTIVE SFCL IN A DISTRIBUTION SYSTEM WITH DG UNITS	International Journal of Electrical and Electronics Engineering Research (IJEER) Vol. 5, Issue 4, Aug 2015, PP.75-82	ISSN(P): 2250-155X; ISSN(E): 2278-943X	6.2879	International
3	DC to AC Multi level Inverter using Bi-directional Switch	International Advanced Research Journal in Science, Engineering and Technology,	ISSN 2394 -1588 vol 3 issue 8	3.943	
4	Optimal Location of Distributed Generator in Radial Distribution System by Using Genetic Algorithm	International Journal of Engineering Trends and Technology (IJETT)	2231- 5381	1.656	

**5.8. Papers presented at National / International Journals:**

S.No	Title of the Paper	Names of the Conference/ Seminars	National/ International	Period
1	LSF based Network Reconfiguration for loss reduction in Radial Distribution System	International Conference on Aero space Electronics, Electrical, Communication and Instrumentation	International	22-10-2016 to 23-10-2016
2	An FPGA based single phase Multilevel Inverter	National Conference on Recent Trends in Power Systems and Drives-2016	National	29-10-2016 to 30-10-2016
3	Optimal placement of Fuel based distribution generation in radial distribution system	ICETEEEM-2012	international	11-12-12 To 12-12-12
4	Experimental results of single phase switched mode power supply	NPSS-2012	National	3-4-12 to 4-4-12
5	Placement of facts devices based on the comparative study of static voltage stability indices	NPEC-2010	National	2-12-10 to 3-12-10

#### 5.9. Sponsored research Projects:

S.No	Title	Agency	Period	Grant amount	Ongoing / Completed
1	Hardware Implementation of FPGA based controller for Multi Level Inverter	UGC	2 years	2,50,000/-	Completed

#### 5.10 Consultancy Projects:

S.No	Title	Agency	Period	Sanctioned Amount	Ongoing / Completed
1.	Nil	Nil	Nil	Nil	Nil

#### 6. Awards / Honors received: Nil

#### 7. Motto: Motto: hard work, discipline and punctuality