



Name:Dr. VENU YARLAGADDA

Designation:Associate Professor, EEE Department

Department: Electrical and Electronics Engineering

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Experience (in years): Teaching: 16 Years Research: 10 Years

Others (If any, Specify): 3 Years Industrial
Experience (APSEB, IDL Industries).

1. Educational / Technical qualifications:

S.No	Level (UG / PG / Ph.D)	Year of passing	Specialization
1	B.Tech: JNTU College of Engineering, Hyderabad	2000	Electrical and Electronics Engineering
2	M.Tech: JNTU College of Engineering, Hyderabad	2006	Electrical Power Engineering
3	Ph.D: JNTU Hyderabad	2015	Power Systems

2. Teaching and Learning:

2.1. Teaching Interests: : Electrical Machines, Power Systems, Power System Analysis, Power System Operation and Control, Flexible AC Transmission Systems, Control Systems, Power System Dynamics, Digital Control Systems, Power Quality and Voltage Stability

2.2. Novel Teaching & Learning Techniques adopted: Expert Videos of specific topic, MATLAB Exercises which will enhances knowledge of students, working models, Demonstrations and Discussions on Recent Paper Publications and Projects carried in the Department etc.

2.3. Involvement in curriculum updating / Design: Yes in many subjects and Labs: Power Systems –II, Electrical Machines I,II and III, PSA,PSOC,FACTS, Control Systems and EM-I,EM-II and CSS, ESS Labs. I have suggested few Modifications in BOS Meetings, which have been implemented in present Curriculum R-13.

3. Co-curricular and Extra-Curricular Activities

3.1. Interests and Hobbies:

- To create positive atmosphere around me to increase work efficiency and perfection.
- To provide motivation for life ambition and to set life to achieve it.
- To provide good attitude & education to our students.

3.2. CCA/ECA Organized:

- Guided the students to get mini projects in various Industries like APSEB, Indian Railways and in various substations
- Guided the students to get papers in various in various conferences

3.3. CCA/ECA participated:

- Motivated and being Motivated the students to have good ambition in life for good performance at studies and to lead a better life.
- Counseled and being counseling the students to improve regularity and punctuality to the classes by telling them necessary reasons

3.4. Counseling and Mentoring Activity:

- Motivating the students towards their achievements of Various Goals like GATE, GRE, M.Tech and Publications of Articles, Presenting papers in Conferences and

participation in the workshops as a result many students got the maximum benefit out of it. Example Three research Articles are Peer Reviewed, Indexed Journals

3.5. Committees involved in:

Department level: DAC and BOS Member

Institute Level: In few committees

4. Conference / Workshop / Seminar / Guest Lectures:

4.1 Conducted:

- Conducted a two week AICTE Sponsored Staff Development Program entitled “Modern Control Techniques for Power Systems” during 18th June 2012 to 30th June 2012 at our premises, B-Block Seminar Hall, VNR VJIET.
- Conducted a Three-Day Workshop on “Simulation of Power Systems Using MiPower Software” for faculty and Research Scholars. Resource Persons are from PRDC, Bangalore, during 25.02.10 to 27.02.10. Venue: Simulation Laboratory, EEE Dept, VNR VJIET, Hyderabad.
- A Two day Workshop on “Developments in Power Systems & Drives” At our premises (VNR VJIET) during last year.
- A Three day Workshop on “ PSCAD / EMTDC Applications in Electrical Engineering” during 5th may to 7th May 2009 at our Premises (VNR VJIET)
- Two-day Workshop on “Modeling and Control of Electrical Machines” for faculty of various Engineering Colleges during 28th & 29th January, 2011.
- Two-Day Workshop on “Developments in Power Systems & Electrical Drives” during 23-24 December 2008 at VNR VJIET.

4.2 Attended: Participated in MATLAB three day workshop conducted during 26th to 28th May, 2014

- A Three-week Refresher course on “simulation Tools for Electrical Engineering Applications” at JNTU, Hyderabad, from 05-12-2005 to 24-12-2005 and obtained grade ‘A’ sponsored by UGC.
- A four-week IEEE Refresher Course on “Advanced control Systems-part1” from 21-7-2006 to 1-8-2006 by Prof. GantiPrasada Rao IEEE fellow at Vasavi College of Engineering, Hyderabad.
- A one-week Quality Improvement Programme conducted by TTTI, Chennai on “Instructional Design&Delivery”from24-06-2002to29-06-2002.
- A tutorial on “Recent Developments in FACTS Technology” by Dr. Kalyan K. Sen at JNTU College of Engineering, Hyderabad, on6-10-2005.
- An IEEE Seminar on “Adaptive and Intelligent Control based Power System Stabilizers” during 24th & 25th of April 2006 at JNTU, Hyderabad, organized by IEEE PES and IAS joint chapter jointly with JNTU, Hyd. and SIST (Sreenidhi) under world bank-TEQIP scheme.
- An IEEE two-day tutorial on “Computational Methods for Large Sparse Power Systems” An Object Oriented approach by Prof. S.A. Soman from IIT Bombay on 12th & 13th of November 2005.
- An IEEE seminar on “Power System Stability and Security” by Prof. Kundur during 2005, at IIIT and JNTU, Hyderabad.
- An IEEE seminar on “Advanced Control Systems” by Prof. GantiPrasada Rao IEEE fellow during sep.2006, Hyderabad. Etc.....
- *** Participated in MORE THAN 30 (Thirty) IEEE Workshops / Workshops / Short Term Intensive Courses etc.

5. Academic Contribution and Research & Consultancy:

5.1. Invited Lectures: Delivered about 8 to 10 Expert Talks in various AICTE Sponsored Workshops, Intensive Courses, workshops and Seminars at our Institution.

5.2. Articles/Chapters published in Books:

Two SPRINGER Book Chapters					
1	PowerSystem Generator and Voltage Stability Enhancement by the HardwareCircuit Implementation of 3-Ph Static Var Compensator (SVC)	Mobile Communication and Power Engineering Communications in Computer and Information Science, Volume 2962013 , pp 465-469, 2013, Springer – Verlag Berlin Heidelberg 2013	ISBN: 978-3-642-35863-0 (Print)	Citation : 18682	International
2	Improvement of system stability margins using coordination control of Static Var Compensator (SVC) and Thyristor Controlled Series Capacitor (TCSC),	Trends in Information, Telecommunication and Computing, Springer science + business media, Newyork 2013 Volume 150, 2013, pp 207-215, Chapter 23,	ISBN: 978-1-4614-3362-0 (Print)	Citation: 18682	International

5.3. Books published as single author or as editor:

1	Implementation of Control Circuit of 1-Ph SVC Using Microcontroller:	International Publication House, Lambert Academic Publishing (LAP) September 16, 2012.	ISBN- 978-3-659-13: 14697-8 ISBN- 10: 3659146978 EAN: 9783659146978	International
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5.4. Projects Guided:

Guided many B.Tech&M.Tech Projects in the areas of Improvement of Voltage Stability, FACTS Controllers, Power System Stability & Control, Design of Electrical Machines, Optima Power Flow Analysis, Fault Analysis, Design, Fabrication & Testing of Static Var Compensators, Optimal placement of FACTS Controllers and Contingency based voltage stability Improvement etc. Presently guiding the projects in the area of Power Systems.

5.5. Research Interests:

Design, Simulation, Control and Fabrication of FACTS Controllers, Power System Dynamic Stability Improvement, Power System Transient Stability Analysis and Methods of Improvement, Voltage Stability Analysis and Improvement, Power Flow Analysis, OPF Problems, Load Frequency Control, Ferro Resonance and SSR Damping Problems, Contingency Analysis and Power System Security, Deregulated Power Systems, Intelligent Systems, Control Systems and Power Quality Issues.

5.6. Ph.D students:

- a) **Enrolled:** Nil
- b) **Submitted:** Nil
- c) **Awarded:** Nil

5.7. Papers published in reviewed Journals:

S. No	Title of the Paper	Journal Name Vol.No. PP	ISBN/ISSN No.	Impact Factor/ Citation Index	National / International
	Hardware Circuit Implementation of Automatic Control of Static Var Compensator (SVC) Using Micro Controller	International Journal of Instrumentation, Control and Automation (IJICA), pp:54-57, Vol.1,Issue.2, 2011.	ISSN : 2231-1890	Citations : 72, H-index: 4	International
2	Voltage Stability Improvement Using Thyristor Controlled Series Capacitor (TCSC) Using Lmn And VCPI Stability Indices”	International Journal of Scientific & Engineering Research IJSER Vol-3, Issue-4 , April-2012	ISSN 2229-5518	Impact Factor 3.2	International
3	“Design And Control Of Small Scale Laboratory Model of A Thyristor Controlled Series Capacitor (TCSC) To Improve System Stability”	International Journal of Scientific & Engineering Research IJSER, Volume 3, Issue 5, May 2012	ISSN 2229-5518	Impact Factor 3.2	International
4	“Automatic Control Of Thyristor Controlled Series Capacitor (TCSC)”	Engineering Research and Applications (IJERA) Vol.2 , Issue 3, May-June 2012, pp.444-449	ISSN 2248-9622	Impact Factor: 1.324	International
5	“Testing And Control of TSC-TCR Type Static Var Compensator (SVC) Using Microcontroller”	International Journal of Control and Automation (IJCA) pp : 277-286 Vol.5,No. 3 September , 2012	ISSN: 2005-4297	Impact Factor: 0.954	International
6	Contingency Based Voltage Stability Improvement”	International Journal of Research in Engineering & Advanced	ISSN: 2320 – 8791	Impact Factor: 2.317	International

		Technology, Control and Automation (IJREAT) ,pp : 338-342 Vol.3, Issue 2 April-May, 2015.			
7	“Comparative analysis of Shunt Compensation Devices Impact on Voltage Stability Enhancement”	IOSR Journal of Electrical and Electronics Engineering (IOSR-JEEE) Volume 10, Issue 3 Ver. III (May – Jun. 2015), PP 54-59	e-ISSN: 2278-1676, p-ISSN: 2320-3331	Impact Factor: 1.419	International
8	Comparative analysis of Fast Acting AVR and ALFC Lops with Classical Controllers on Dynamic Stability Improvement”	International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering(IJAREEI E) Vol. 4, Issue 6,Pp:5680-5693, June 2015	ISO 3297: 2007, ISSN (Print) : 2320 – 3765, ISSN (Online): 2278 – 8875,	Impact Factor : 5.016	International

5.8. Papers presented at National / International Journals:

S.No	Title of the Paper	Names of the Conference/ Seminars	National/ International	Period
1	“Power System Generator And Voltage Stability Enhancement By The Hardware Circuit Implementation of 3-Ph Static Var Compensator (SVC)”	Third International Conference on Control, Communication and Power Engineering : CCPE 2012	International Organized by ACEEE –IDES USA	27 th & 28 th April-2012
2	Dynamic Stability Improvement With Combined Fast Acting Automatic Voltage Regulator (AVR) And Automatic Load Frequency Control (ALFC) Loops	ICMAET, pp 93-96.	International	1-1-2012 to 2-1-2012
3	Voltage Stability Improvement using Design and Implementation of Synchronous Phase Modifier (SPM) And Finding Its Optimal Location In Power Systems For Voltage Stability Improvement	International Conference On Electrical And Electronics Engineering ICEEE ,Pp – 44-48,ISBN: 978-93-81693-55-1	International	21-22 April-2012
4	Improvement of System Stability Margins Using Coordination Control Of Static Var Compensator (SVC) And	Third International Conference on Advances in Power Electronics and	International Organised by ACEEE, IDES USA	Aug 03-04, 2012

	Thyristor Controlled Series Capacitor (TCSC)	Instrumentation Engineering - PEIE 2012		
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5.9. Sponsored research Projects:

- Conducted a two week AICTE Sponsored Staff Development Program entitled “ Modern Control Techniques for Power Systems” during 18th June 2012 to 30th June 2012 at our premises, B-Block Seminar Hall, VNR VJIET with AICTE Grant of Rs.3,75,000/-

5.10 Consultancy Projects: Nil

6. Awards / Honors received: Nil (But Good Achievements are:

1.Secured State 7th Rank in E-CET 1996 & 2. All India Rank 274 in GATE-2001)

7. Motto: To inculcate genuine Subject Knowledge into the young brains with Human Values. To carryout remarkable research in the field of Electrical Engineering and to publish the work in indexed, referred and cited Journals with good Impact Factors.