

Name: B. DEVULAL

Designation: Assistant Professor

Department: Electrical and Electronics Engineering

Mail.I.D: devulal_b@vnrvjiet.in

Experience (in years): Teaching: 4 years **Research:** Nil **Industrial training:** 6 months **industrial exp:** 1 year



1. Educational / Technical qualifications:

S.No	Level (UG / PG / Ph.D)	Year of passing	Specialization
1	B.E In Sir C R Reddy College of Engineering, Andhra university Eluru	2009	Electrical and Electronics Engineering
2	M.tech in JNTU K College of Engineering, Kakinada	2011	High Voltage Engineering

2. Teaching and Learning:

2.1. Teaching Interests: Power system Analysis, Electrical Distribution Systems, Electrical Machines, Network Analysis, Reliability Engineering and its Application to Power Systems, Principles of Electrical Engineering

2.2. Novel Teaching & Learning Techniques adopted: WIT and WILL

2.3. Involvement in curriculum updating / Design: Nil

3. Co-curricular and Extra-Curricular Activities

3.1. Interests and Hobbies: To conduct various activities for students to enhance their knowledge

3.2. CCA/ECA Organized: Nil

3.3. CCA/ECA participated: Involved in Career vision activities for First year B.Tech students

3.4. Counseling and Mentoring Activity: Mentoring students for regularity and to improve academic performance

3.5. Committees involved in:

Department level: Nil

Institute Level: placement coordinator for EEE Dept

4. Conference / Workshop / Seminar / Guest Lectures:

4.1 Conducted: Nil

4.2 Attended: National conference participated on power system.

5. Academic Contribution and Research & Consultancy: Nil

5.1. Invited Lectures: Nil

5.2. Articles/Chapters published in Books: Nil

5.3. Books published as single author or as editor: Nil

5.4. Projects Guided: final year students B.tech and M.tech

(a) **UG:** "High gain single stage boosting inverter for photo voltaic applications" The proposed topology employs a tapped inductor to attain high input voltage step-up and consequently allows operation from low dc input voltage .

(b) **PG:** power quality issues in micro grid at various stage.

5.5. Research Interests: power system, Renewable Energy Sources (RES) and High voltage Engineering.

5.6. Ph.D students:

a) Enrolled:Nil

b) Submitted:Nil

c) Awarded:Nil

5.7. Papers published in reviewed Journals: Nil

5.8. Papers presented at National / International Journals:

S.No	Title of the Paper	Names of the Conference/ Seminars	National/ International	Period
1	Nil	Nil	Nil	Nil

5.9. Sponsored research Projects:Nil

5.10 Consultancy Projects:

S.No	Title	Agency	Period	Sanctioned Amount	Ongoing / Completed
1.	Nil	Nil	Nil	Nil	Nil

6.Awards / Honors received:

1. Secured second prize in Kho-Kho at District level.
2. Secured second prize in Volley ball at Zonal level.
3. Achieved merit scholarship in M.Tech level.

7.Motto:DisciplineHard work, sincerity, simplicity