

Name: **Dr. V Sagar Reddy**

Designation: Assistant Professor

Department: Electronics and Communication Engineering.

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Experience (in years): 10 Teaching: 08 Research: Nil Others
(Industry) : 01



1. Educational / Technical qualifications:

S. No	Level (UG / PG / Ph.D)	Year of passing	Specialization
1	Ph.D	2021	VLSI Signal Processing
2	M.Tech	2015	Embedded Systems
3	B.Tech	2011	Electronics and Communication Engineering.

2. Teaching and Learning:

2.1. Teaching Interests: Electronic Devices and Circuits, Low Power VLSI, Analog Circuits, Control Systems, Signals and Systems, Machine Learning & AI, Deep Learning

2.2. Novel Teaching & Learning Techniques adopted: POGIL, WIT & WIL and Learning by Doing, Course based projects

2.3. Involvement in curriculum updating / Design: B.Tech (EDC & Analog Circuits)

3. Co-curricular and Extra-Curricular Activities

3.1. Interests and Hobbies: Playing Badminton and cricket.

3.2. CCA/ECA Organized:

1. Organized one week workshop on "IOT and Embedded Systems" during 20th - 25th Feb. 2017 in collaboration with FICE Educational Institute, Bangalore.

2. Organized IOT certificate courses.

3.3. CCA/ECA participated:

Member IEEE, Fellow of IETE, Participated in Various Sports fests.

3.4. Counselling and Mentoring Activity: Counselling the needy students and monitoring their progress in attendance as well in performance

3.5. Committees involved in:

Department level:

- M. Tech ES Project File In-Charge
- Member of Project Review Committee (PG – Embedded Systems).
- Class Coordinator

Institute Level: Nil

4. Conference / Workshop / Seminar / Guest Lectures :

4.1. Conducted: 2

1. Organized one week workshop on "IOT and Embedded Systems" during 20th - 25th Feb. 2017 in collaboration with FICE Educational Institute.

2. Conducted IOT Boot Camp during summer vacation 2017-18.

4.2. Attended: 10

• FDP201x, "Pedagogy for Online and Blender Teaching-Learning Process" conducted by IITB. 30-10-2018 to 13-12-2018.

• FDP101x, "Foundation program in ICT for Education" conducted by IITB. 13-09-2018 to 18-10-2018.

• FDP on "Recent Trends in Signal Processing and Its Applications" conducted by NITW 22nd June to 1st July, 2017.

- Attended FDP on “Recent Trends in Signal Processing and applications” organized by E & ICT Academy, NIT Warangal, from 22nd June to 1st July 2017.

5. Academic Contribution and Research & Consultancy:

- 5.1. Invited Lectures: Nil
- 5.2. Articles/Chapters published in Books: Nil
- 5.3. Books published as single author or as editor: Nil
- 5.4. Projects Guided:
 - a) UG: 08 b) PG: 02
- 5.5. Research Interests : Signal Processing and Speech Processing
- 5.6. Ph.D students : Nil
 - a) Enrolled :
 - b) Submitted:
 - c) Awarded:
- 5.7. Papers published in reviewed journals: 04

S.No	Title of the paper	Name of author/s	Name of the Journal	UGC Notified (Y/N)	Year of Publication	ISBN/ISSN Number Impact factor
1	Modified Nonlocal Means Filtering for Speech Enhancement and Its FPGA Prototype	Sagar Reddy V and Bikshalu K.	Circuits, Systems, and Signal Processing (SCIE)	Y	2021	1.670
2	Real-time speech enhancement using optimised empirical mode decomposition and non-local means estimation	Sagar Reddy V and Bikshalu K.	IET Computers & Digital Techniques (SCIE)	Y	2021	1751-8601 0.785
3	Nonlocal means estimation of intrinsic mode functions for speech enhancement	Sagar Reddy V and Bikshalu K.	Turkish Journal of Electrical Engineering and Computer sciences (SCIE)	Y	2020	(2020) 28 0.703
4	Speech Enhancement using Discrete Wavelet Transform and Nonlocal Means Estimation	Sagar Reddy V and Bikshalu K.	Jour of Adv Research in Dynamical & Control Systems	Y	2019	Vol. 11, 04 - Special Issue, pp: 1834-1840

5	Design and Development of accelerometer based System for driver safety	V. Sagar Reddy, Dr.L.Padma Sree, V. Naveen Kumar.	International Journal of Science, Engineering and Technology Research (IJSETR)	Y	2014	2278 – 7798 3.59
6	Bandwidth Extension of Speech using Data Hiding Method	V. Sagar Reddy, P. Srinivasa Rao.	Journal of Emerging Technologies and Innovative Research (JETIR)	Y	2016	2349-5162 5.87
7	Dual-threshold Single-ended Schmitt-Trigger Based Radiation Hardened memory Design with Fault modeling System	V. Sagar Reddy Ganesh Chokkakula, Naresh.K.	IOSR journal of VLSI and Signal Processing (IOSRJVS P)	Y	2017	2319 – 4197 2.82
8	On Line English Character Recognition using Raspberry Pi	V. Sagar Reddy, CH. Paveen Kumar, Dr.V. Padmaja, CH. Nagadeepa.	International Journal of Science, Engineering and Technology Research	N	2017	2319-8885 5.762

5.8. Papers presented at National / International Conferences: Nil

S.No	Title of the Paper	Names of the Conference/ Seminars	National/ International	Period
1	Greenhouse environment monitoring and automation using Intel Galileo gen2 and IOT	ICWSNUCA – 2018	International	24 – 25 th August 2018
2	Speech enhancement using DWT and NLM	ICRCET- 2018	International	31 st JAN- 01 st FEB 2018

5.9. Sponsored research Projects: Nil

S.No	Title	Agency	Period	Grant amount	Ongoing / Completed

5.10 Consultancy Projects: Nil

S.No	Title	Agency	Period	Sanctioned Amount	Ongoing / Completed

6.Awards / Honors received:

- Qualified UGC-NET and GATE Exam (2012,2013 and 2018)
- Got ELITE in NPTEL online certification course in Networks and Systems

7.Motto: You may fall so many times, but always stand up.