

Name: **N NEELIMA**

Designation: Assistant Professor

Department: Electronics and Communication Engineering Department

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Experience (in years): 14 Teaching: 14

Research: --

Others(if any,

specify): ---



1. Educational / Technical qualifications:

S.No	Level (UG / PG / Ph.D)	Year of passing	Specialization
1	Ph.D	pursuing	
2	M.Tech	2006	VLSI System Design
3	B.Tech	2002	ECE

2. Teaching and Learning:

2.1. Teaching Interests:

Electronic devices & Circuits, Switching Theory and Logic Design, Digital IC Applications, Microprocessor & microcontrollers, Computer organization, Basic Electronics, Digital Image processing, Digital system design, VLSI, Computer Networks.

2.2. Novel Teaching & Learning Techniques adopted: Videos and PPT

2.3. Involvement in curriculum updating / Design: NIL

3. Co-curricular and Extra-Curricular Activities

3.1. Interests and Hobbies: Playing Badminton, Dancing, Crafts work, Painting etc.

3.2. CCA/ECA Organized: NIL

3.3. CCA/ECA participated: NIL

3.4. Counselling and Mentoring Activity: NIL

3.5. Committees involved in:

Department level: Member in NBA verification team

Institute Level: NIL

4. Conference / Workshop / Seminar / Guest Lectures :

4.1 Conducted: NIL

4.2 Attended:

- A three day workshop on “ Numerical methods in Data Science and its functions executed with Sci-Lab”, during July 4th -july 6th ,2019 organized by Department of science and Humanities, CMRIT, Hyderabad.
- A two week Training programme on “Industrial Awareness cum Workshop on PCB”, during March 10th -March 24th , held at NSIC, Hyderabad.
- A one week workshop on “ CMOS, Mixed Signal and Radio Frequency VLSI Design”, conducted by IIT Kharagpur, during Jan 30th –Feb 4th ,2017 held at MRCET, Hyderabad.
- A one-week FDP on “Analog and Digital IC Design-Hands on Experience using Cadence Tools”, during Nov 26th – Dec 1st 2015, held at CMRCET, Hyderabad.
- A 3-day workshop on “Rough sets approach for engineering applications”, during Dec 2-4, 2013 held at MRCET, Hyderabad.
- Two-week workshop on “Real time Embedded system Development” during Nov 18-30, 2013 held at VNR VJIET, Hyderabad.

- A Two day workshop on “Analog system design using TIASLKV2010” during July 30-31,2013 held at MRITS, Hyderabad.
- Two week workshop on ”Analog Electronics” conducted by IIT Karaghpur, during June 4-14,2013 held at MRCET, Hyderabad.
- One week AICTE sponsored workshop on “Advanced VLSI System Design”,during June 17-29,2013 held at geethanjali college of Engg. & Tech., Hyderabad.
- A 3 day workshop on “Full custom IC design & FPGA design flow” during Feb 23-25,2013 held at VNRVJIET.
- One day seminar on “MATLAB & SIMULINK for Engg. Education”,conducted by Mathworks on 31AUG 2012 at Hyderabad.
- A 3 day FDP programme on “ Analog & Digital CMOS IC Design using Mentor Graphics EDA tools” during July19-21,2012 held at MRITS, Hyderabad.
- One day workshop on “Moderanization Aspects of GNSS and its applications”, organized by Research and Training unit for Navigational Electronics, OU Hyderabad on 17 Dec 2011.
- One day FDP on “Image Processing” during 16 july 2011, held at Vignan Institute of technology & science, Hyderabad.
- A 3 day FDP on “Latest trends in VLSI Design using FPGA/CPLD and Soc “ from 31st July to 2nd August,2010 held at MRITS, Hyderabad.
- A 3 day FDP on “Implementation of DSP Algorithms using MATLAB “ during Feb 20-22,2008 at MRITS, Hyderabad.

5. Academic Contribution and Research & Consultancy:

5.1. Invited Lectures: NIL

5.2. Articles / Chapters published in Books: NIL

5.3. Books published as single author or as editor: NIL

5.4. Projects Guided :

a) UG : 15 b) PG : 7

5.5. Research Interests :

VLSI and Image Processing

5.6. Ph.D students :

a) Enrolled : - b) Submitted : - c) Awarded : -

5.7. Papers published in reviewed journals :

S.No	Title of the Paper	Journal Name Vol.No. PP	ISBN/ISSN No.	Impact Factor/ Citation Index	National/ International
1	Implementation of face and eye detection on DM6437 board using Simulink model	Bulletin of Electrical Engineering and Informatics, Vol-9, no2	23029285 20893191	1.90	International

2	Tone Mapping High dynamic Range images by Hessian Multiset Canonical Correlations	sensing and Imaging, springer,21,artcicle no-8	15572072 15572064	1.158	International
3	Optimal Tone Mapping for HDR Images	IJRTE sept 2019 Vol 8	2277-3878	7.957	International
4	Optimal clustering based outlier detection and cluster center initialization algorithm for effective tone mapping	Multimedia tools and applications Volume 67, Number3, July-2019	1380-7501	2.757	International
5	Performance of Assessment of TMOs	Procedia computer science 89 (2016), 700-709	1877-0509	0.883	International
6	Tone Mapping Operators for Conversion of HDR Images to LDR Images-A Review	IJCCS, pg 175-181, Vol4,issue2	2277-6699	-	International
7	Development of RF Test Data Modulator using Programmable Frequency Synthesizer	IJSETR Vol-2,Issue-18,Dec-2013	2319-8885	5.762	International
8	Implementation of product reed Solomon code	IJERT Vol-2,Issue-1,Jan-2013	2278-0181	-	International
9	Design and development of warning system for drowsy drivers	IJSRP Vol-3,Issue-11,Nov-2013	2250-3153	7.662	International
10	Design and development of Speed	IOSR	2278-2834	3.12	International

	controller for drowsy drivers	Vol-8, Issue-2, Nov-Dec, 2013			
11	Development of control algorithm for Ring Laser Gyroscope	IOSR), Vol-3, Issue-4, sep-oct, 2012	2278-2834	3.12	International

5.8. Papers presented at National / International Journals :

S.No	Title of the Paper	Names of the Conference/Seminars	National/ International	Period
1	A Comparative review of different TMOs for HDR images	ICICS-2020	International	June-26-27, 2020
2	TMQI and structural fidelity for Tone Mapped Images	ICRIT-2018	International	21-22 Dec 2018
3	A Two State Hybrid Operator for Tone Mapping HDR Images	ICACNI-2017 NIT-Goa	International	1-3 June 2017
4	Performance Assessment of TMOs	ICISP-2016	International	August 19-21
5	Modified Ashikhmin Tone Mapped Operator for Conversion of HDR images to LDR images	ICEEE-2016	International	29 th Feb-2 nd March, 2016
6	A New Ashikhmin Based Operator for Generating Tone Mapped Images	IEEE sponsored 3 rd international Conference on Electronics and Communication Systems	International	25-26, Feb-2016
7	Object Detection and Tracking- A Survey	CICN-2015	International	12-14 December 2015.

8	A Tone Mapping Operators for conversion of HDR Images to LDR Images - A Review	ICCSPCIT-2015	International	DEC 18-19,2015
9	Modified Ashikhmin Tone Mapped Operator for Advanced HDR Images	VSPICE2K15	National	6-7,November,2015
10	Performance Improvement of SOI over Bulk CMOS	NCIIT-2010	National	24-25 March 2010
11	Texas instruments India Educators Conference 2013	Texas instruments india university program	National	April 4-5, 2013

5.9. Sponsored research Projects: NIL

S.No	Title	Agency	Period	Grant amount	Ongoing / Completed

5.10 Consultancy Projects: NIL

S.No	Title	Agency	Period	Sanctioned Amount	Ongoing / Completed

6. Awards / Honors received:

- Awarded Gold medal for M.Tech Academic excellence for the academic year 2004-2006.
- Stood second during the B.Tech Program.
- Received Guinness world of record certificate for participating in silicon Andhra-2016
- National Kho-Kho player
- Won first prize for badminton during faculty sports fest conducted in MRITS for years 2014-2016.
- Finalist in Mrs India Telangana 2021.
- Received Mrs Practical 2021 and Best theme award during MrsIndia Telangana 2021.
- Received Nritya Jyothi title at Naveen Kalakar, Odissa for Kuchipudi

7. Motto: Its one life and we need to make the most of it.