

Name: **A. Lavanya**

Designation: Assistant Professor

Department: Electronics and Communication Engineering.

Mail ID: lavanya\_a@vnrvjiet.in

Experience (in years): 7 years Teaching: 6 years 3 months

Research: NIL Others (if any, specify):



### 1. Educational / Technical qualifications:

S.No	Level (UG / PG / Ph.D)	Year of passing	Specialization
1	Ph.D	Pursuing	Low Power Memories
2	M.Tech	2015	VLSI SYSTEM DESIGN
3	B.Tech	2011	Electronics and Communication Engineering.
4	Intermediate	2007	MPC
5	S.S.C.	2005	

### 2. Teaching and Learning:

2.1. Teaching Interests: Microprocessors and Microcontrollers, LIC, PDC, STLD, CPLD, VLSI

2.2. Novel Teaching & Learning Techniques adopted: POGIL, WIT & WIL, Project Based learning

2.3. Involvement in curriculum updating / Design:

### 3. Co-curricular and Extra-Curricular Activities

3.1. Interests and Hobbies: Sports, Listening to music.

3.2. CCA/ECA Organized: ECE co-coordinator for open house project expo 2016-17

3.3. CCA/ECA participated:

3.4. Counselling and Mentoring Activity: Mentor for 1<sup>st</sup> and 2<sup>nd</sup> B. Tech students

3.5. Committees involved in:

Department level: Anti ragging and Discipline committees

Institute Level: Nil

### 4. Conference / Workshop / Seminar / Guest Lectures :

4.1. Conducted:

4.2. Attended:

- Two weeks AICTE Sponsored Workshop on "Trends in Wireless communication Evolution of Simulation Tools" conducted from 11<sup>th</sup> May to 23<sup>rd</sup> May 2015.
- Participated FDP on "Recent Trends and Future applications in Electronics and communication technology" conducted by MLR Institute of Technology from 29/05/2020 to 02/06/2020.
- Participate Faculty Development Programme on "System Design Methodologies for Embedded, IoT, AI, & HPC using Intel FPGA" to be held from 19/04/2021– 30/04/2021 jointly organized by Electronics & ICT Academies at IIT Guwahati, NIT Patna, MNIT Jaipur and NIT Warangal.

### 5. Academic Contribution and Research & Consultancy:

5.1. Invited Lectures: Nil

5.2. Articles/Chapters published in Books: Nil

5.3. Books published as single author or as editor: Nil

5.4. Projects Guided:

a) UG: NIL

b) PG: NIL

5.5. Research Interests :Low power VLSI

5.6. Ph.Dstudents :

a) Enrolled : NIL

b) Submitted: NIL

c) Awarded: NIL

5.7. Papers published in reviewed journals: 1

S.No	Title of the Paper	Journal Name Vol.No. PP	ISBN/ISSN No.	Impact Factor/ Citation Index	National/ International
1.	Mixed signal implementation of carry select adder for low power applications	International journal of computer Science information and Engineering Technologies(IJCSIE T)	ISSN(online):2277-4408 Volume 3-Series 2 Issue 4,Oct 2014	2.9	International Journal

5.8. Papers presented at National / International Conferences:

S.No	Title of the Paper	Names of the Conference/ Seminars	National/ International	Period

5.9. Sponsored research Projects: Nil

S.No	Title	Agency	Period	Grant amount	Ongoing / Completed

5.10 Consultancy Projects: Nil

S.No	Title	Agency	Period	Sanctioned Amount	Ongoing / Completed

6. Awards / Honors received: Nil

7. Motto: Work is worship.