

VALLURUPALLI NAGESWARA RAO VIGNANA JYOTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

DEPARTMENT OF ELECTRONIC AND COMMUNICATION ENGINEERING
M. Tech (EMBEDDED SYSTEMS)





Brief view of the Programme

- Programme Name: M. Tech (Embedded Systems)
- Duration: 2 Years (4 Semesters)
- Offered by the Department: ECE
- Programme offered since: 2006
- Sanctioned Intake: 18



About the Programme

Master of Technology in Embedded Systems is a two post-graduate programme dedicated year enlightening students with in-depth knowledge on Embedded systems, its control, real-time systems, model-based design and verification, **PCB** level integration, processor architecture and the applications of embedded systems in real-life. The programme offer students with a strong base of Embedded Systems principles and applications in scientific and engineering domains

Objectives of the Programme

- AT.
- Demonstrate outstanding analytical & technical skills to evaluate, analyze and solve real time problems in Embedded Systems.
- Apply the acquired knowledge to solve engineering problems to suit multidisciplinary situations.
- Undertake research and development projects in the field of Embedded Systems.
- Continue the personal development through professional study and self-learning.
- Demonstrate their professional, ethical and social responsibilities and contribute their part for
- addressing various global issues.

Training & Placement

Placement Summary/ Year	2021-23	2020-22	2019-'21
Eligible Students	07	13	12
Placed Students	02 (still in progress)	09	07
Highest Package	8LPA	6LPA	6LPA





Our Prominent Recruiters











Mahind



Infrastructure

Embedded Laboratory



- **THUNDER BOARD**
- Raspberry PI BOARD

Research projects sponsored by ITRA, DRDO, AICTE, UGC



MOU with Industries









Weapon Locking and Tracking System









ALLUMINI



Phani Design Engineer Silicon Labs



Shubha Deepthi R&D Engineer Salcit Technologies



Naveena A S Engineer PEGASYS



Nikhil Anand Associate Engineer BOSCH



Navaneetha 3r. Embedded Engineer Jtthunga Technologies



Sharvani Research Scholar BITS Hyderabad