Name: **Dr. V. Pradeep Kumar**
Designation: Assistant Professor
Department: Electronics and Communication Engineering
Mail I'd: pradeepkumar_v@vnrvjiet.in

Experience (in years): Teaching: 11.5 Research: Industry:02

1. Educational / Technical qualifications:

S.No	Level (UG / PG / Ph.D)	Year of passing	Specialization
1	Ph.D	2023	Device Modelling
2	M.Tech	2008	VLSI Systems Design
3	B.Tech	2004	ECE

2. Teaching and Learning:

2.1. Teaching Interests: VLSI Design, Signals and Systems, Linear digital integrated circuits, Analog and digital communication, Switching Theory and Logic Design, Digital Signal Processing, Micro Processor and Micro Controller, MoS Circuits.

2.2. Novel Teaching & Learning Techniques adopted: WIT & WIL, Think pair share, Online teaching methods, and Learning by Doing.

2.3. Involvement in curriculum updating / Design: Yes

3. Co-curricular and Extra-Curricular Activities [In VNR VJIET]

3.1. Interests and Hobbies: Sports → Cricket, badminton

3.2. CCA/ECA Organized: VLSI Club Coordinator

3.3. CCA/ECA participated: Not yet Started.

3.4. Counseling and Mentoring Activity: Not yet Assigned.

3.5. Committees involved in

Department level:

1. Software Tools Training
2. VLSI Club Coordinator

Institute Level:

3. Conference / Workshop / Seminar / Guest Lectures:

3.1 Conducted: 03

3.2 Attended: 08

4. Academic Contribution and Research & Consultancy:

5.1. Invited Lectures:

5.2. Articles / Chapters published in Books: 02

5.3. Books published as a single author or as editor: Nill
5.4. Projects Guided:

a) UG/ PG: 14

5.5. Research Interests: Modelling and Simulation Nano Devices, Bio-Sensing, Nanoelectronics and Nanotechnology, VLSI Design and Technology.

5.6. Ph.D students: NA

a) Enrolled: b) Submitted: c) Awarded:

5.7. Papers published in reviewed journals: 04

5.8. Papers presented at National / International Journals:02

5.9. Sponsored research Projects:00

5.10 Consultancy Projects:00

5. Awards / Honors received:

6. **Motto:** IT AIN'T OVER TILL IT'S OVER