Name: **Dr. C.D. NAIDU**

Designation: Principal & Professor

Department: Electronics & Communication Engineering

Mail.ID: cdnaidu@vnrvjiet.in

Experience (in years): Teaching: 36 Years   Research: 15 Years

Others (If any, Specify):

- Principal since 2016 to till date at VNR VJIET, Hyderabad.
- Director PG Studies (2015 to 2016) at VNRVJIET, Hyderabad
- Principal (from 2009 to 2015) at VNRVJIET, Hyderabad
- Vice Principal (2006 to 2009) at VNRVJIET, Hyderabad.
- Dean, Academics (2008 to 2009) at VNRVJIET, Hyderabad.
- Professor & Head (from 2002 to 2009) at VNRVJIET, Hyderabad.
- Associate Professor (1997 to 2002) at VNRVJIET, Hyderabad.
- Sr. Lecturer/ Assistant Professor (1990 to 1997) at Vasavi College of Engineering, Hyderabad.
- Lecturer (1985 to 1990) at KSRM College of Engineering, Kadapa.

1.Educational / Technical qualifications:

S. No	Level (UG/ PG/ Ph.D.)	Year of passing	Specialization
1	Ph. D.	2002	Image Processing Applications Through Dynamic Neural Network with Reuse of Pruned Nodes
2	M. Tech.	1985	Instrumentation and Control Systems
3	B.Tech.	1982	ECE
4	Intermediate	1978	MPC
5	High School Education	1976	SSC

Membership in Professional Societies

- Indian Society for Technical Education, Life Member
- IEEE Member
- VLSI Society of India Member
- Fellow IETE

2. Teaching and Learning:

2.1. Teaching Interests: Signals and Systems, Digital Signal Processing, Control Systems, Digital Systems, Image Processing, Neural Networks.

2.2. Novel Teaching & Learning Techniques adopted: WIT& WIL; POGIL

2.3. Involvement in curriculum updating / Design: Yes

3. Co-curricular and Extra-Curricular Activities

3.1. Interests and Hobbies:

- Interest in Teaching, helping students who are slow learners and encourage students to study and work hard.

3.2. CCA/ECA Organized: Conducted many students cultural, annual day, sports day functions.

3.3. CCA/ECA participated: Involved in conduct many student fests at VNRVJIET.

3.4. Counselling and Mentoring Activity:
- Counselled many students for pursue higher studies to overcome their learning inhabitations.

3.5. Committees involved in:

Department level: BoS; DAC; PAC.

Institute Level: Chairman IAC; Chairman IQAC; GC; FPDC; Chairman Disciplinary Committee at VNRVJIET.

4. **Conference / Workshop / Seminar / Guest Lectures**:

4.1 Conducted:
- Workshop on MATLAB AND TOOLBOXES from April 17-20, 2009.
- 3-day short term Intensive course on "Embedded Architecture & Applications" from Feb 12-14, 2009.
- Three-day Workshop on Digital Signal Processing and Embedded Systems, by Dept of ECE & VSI Chapter from August 18-20, 2008.
- Two-day workshop on Low Power Design and Test at VNRVJIET in Association with VSI, C-DAC, Hyderabad from July 30-31, 2007.

4.2 Attended:
- Conference EDGE-2009 Organized by EDGE Forum held at India habitat centre, New Delhi from February 09-11, 2009.
- Attended 21st International Conference on VLSI Design, 7th International conference on Embedded Systems organized by VSLI DESIGN-2008 HICC Madhapur, from January 04-08, 2008.
- V. Nagesh, and C. Dhanunjaya Naidu, Investigation of a DFT-IDFT based Approach to Fractional Sampling Rate Conversion, National Conference on signals Systems and Security, Department of ECE, PSG College of Technology, Coimbatore, India, March 1-2, 2002.
- Arun K. Pujari, C. Dhanunjaya Naidu, and B.C. Jinaga An adaptive Character Recognizer for Telugu Scripts Using Multi- Resolution Analysis and Associative Memory, Third Indian Conference on Computer Vision, Graphics and Image Processing, Space applications centre, Ahmedabad, December 2002.
- M. SrinivasaRao, J. Anand Chandulal, K. Venkateswara Rao, and C. Dhanunjaya Naidu, VLSI Implementation of Dynamical Neural Network (DNN), National Symposium on Low Power VLSI Design, Hyderabad, February 2003.

5. **Academic Contribution and Research & Consultancy:**

5.1. Invited Lectures:

5.2. Articles/Chapters published in Books:

5.3. Books published as single author or as editor:

5.4. Projects Guided:

    a) UG: 54 projects in the areas of Digital Signal Processing, Circuit Design, Neural Networks, Image Processing, VLSI domain.

    b) PG:14

5.5. Research Interests: Digital Signal Processing, Digital Filters, Neural Networks, Wavelet Transforms, Image Processing & Analysis.

5.6. Ph. D. students: Presently guiding a Ph.D. student

- B. Tech and M. Tech projects are guided in the areas of DSP, ZIGBEE processors, VLSI and Image Processing.
  - a) Enrolled: 8
  - b) Submitted: 1
  - c) Awarded: 5

5.7. Papers published in reviewed Journals:

S. No	Title of the Paper	Journal Name Vol. No. PP	ISBN/ISSN No.	Impact Factor/ Citation Index	National/ International
1.	An Intelligent Character Recognizer for Telugu Scripts Using Multi Resolution analysis and Associative Memory	Journal of Image, Vision Computing		pp. 1221 - 1227	
2.	Order Sensitive Learning in Hopfield Neural Network	International Conference on Systemics, Cybernetics and Informatics, Vol 1,pp 227-231			International
3.	Human signature verification using Dynamic Neural Network (DNN) With Enhanced Capacity by Reutilization of pruned Nodes	International Conference on Systemics, Cybernetics and Informatics, Vol 1,pp 627-633			International

4.	Design of loop filter for synchronous Pseudolite Navigation system using PLL	International Conference on Systemics, Cybernetics and Informatics, Vol 1,pp 87-90			International Conference
5.	Trade Off for the Compression of Multispectral and Hyper Spectral Images – Literature Survey	pp 13–21, Elsevier Publications 2014			International Conference
6.	FPGA Implementation of Multi Parameter Deinterleaving	(ICCCI – 2014), Jan. 03 – 05, 2014,			International Conference
7.	Optimal Coded Sequence Design for Radar using Fractional Oppositional Particle Swarm Optimization	ICRAIE-2014			International Conference
8.	Real Time Multi Spectral Image Compression for High Resolution Imageries through Super Resolution algorithm with DWT-ISPIHT	International Conference on Electrical, Electronics, Signals, Communication and Optimization (EESCO)	ISBN 978147 997678 2/15		International Conference
9.	Fuzzy Based Super Resolution Multispectral Image Compression with Improved SPIHT	International Conference on Communication and Signal Processing	ISBN 978-1-4799-8080-2		International Conference
10.	UWB Technique for Detection of Live Buried Persons under Debris	International Conference on Recent	pp 1-6 ,		International Conference

		Trends in Engineering & Technology			
11.	UWB Propagation Modeling of Human being behind a Concrete wall for the study of Cardiac Condition	IEEE International Conference on Electrical, Electronics, signals, communication, and Optimization (EESCO	pp 1222-1226		International Conference
12.	Modelling of human throax and study on human heart activity with UWB radar from UHF to S-Band	IEEE, International Conference on CSPACES	pp 370-374		International Conference
13.	Designing Algorithms for high Resolution Multispectral Image Compression with Improved DWT SPIHT	International Conference on Innovations in Electronics & Communication Engineering (ICIECE-2015)	ISBN: 978-93-85100-41-3		International Conference
14.	Sparse Recovery Algorithms Based on Dictionary Learning for MR Image Reconstruction	IEEE WiSPNET 2016 conference	978-1-4673-9338-6/16		International Conference
15.	An Intelligent character Recognizer for Telugu scripts using multi-resolution analysis and associative memory	Image and vision computing Journal	1221-1227 Vol 22	26	International

16.	Mathematical Modelling and FPGA Implementation of Particle Swarm Optimization	International Journal of Scientific Engineering and Technology Research	ISSN: 2319 – 8885	pp 2003-2007	International
17.	FPGA based Solution for the Identification of Radar Pulse Sequences for Defense Applications	Global Journal of Researches in Engineering	ISSN: 2249-4596 Print ISSN: 0975-5861		International
18.	Design and implementation of Novel SPIHT algorithm for multispectral image compression	International Journal of Applied Engineering Research,	ISSN 0973-4562		International
19.	Highly Scalable compression Method for Super Resolution Multi Spectral images	”, IOSR Journal of VLSI and Signal Processing (IOSR-JVSP) Volume 4, Issue 3, Ver.I, PP 01-15	e-ISSN: 2319 – 4200, p-ISSN No.: 2319 – 4197		International
20.	Modelling of Propagation Losses for Human Being Behind a Brick Wall	International Journal of Engineering Science and Technology	ISSN: 0975-5462		International
21.	Study of RF Propagation Losses in Homogeneous Brick and Concrete Walls using Analytical Frequency Dependent Models	IOSR Journal of Elect ronics and Communicat ion Engineering	e-ISSN:2 278-2834, P-ISSN:2 278-8735		International

22.	Radar Based Lie Detection Technique	Global Journal of Researches in Engineering: Electrical and Electronics Engineering Online	ISSN:2 249-4596 P-ISSN:0 975-5861		International
23.	Frequency Dependent Planar Electromagnetic Modelling of Human Body and Theoretical Study on Attenuation for Power Budget Estimation of UWB Radar	Global Journal of Researches in Engineering: F, Electrical and Electronics Engineering	Online ISSN:2 249-4596 P-ISSN:0 975-5861		International
24.	Study of RF Signal Attenuation of Human Heart	Journal of Engineering, Hindawi Publishing Corporation	Vol 2015		International
25.	Multispectral Image Compression for various band images with High Resolution Improved DWT SPIHT	International Journal of Signal Processing, Image Processing and Pattern Recognition	ISSN: 2005-4254		International
26.	Multispectral Image Compression with High Resolution Improved SPIHT for testing various input images	Communicat ed a paper to i-manager's Journal on Image Processing (JIP)	Print: 2 349-4530 ISSN Online: 2349-6827		International

5.8. Papers presented at National / International Conferences: 28

5.9. Sponsored research Projects: 4

5.10 Consultancy Projects: 0

6. Awards / Honours received: 1
- Best Principal Award
- No of Patents – 2

7. **Motto**: There is no substitute for hard work.