

Name : Mr. CHIRANJEEVI THOKALA

Designation: Assistant Professor

Department: Electronics & Communication Engineering

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Experience (in years): 6 Teaching: 5 Research: 0 Others (Industry): 1

1. Educational / Technical qualifications:

S.No	Level (UG / PG / Ph.D)	Year of passing	Specialization
1	Ph.D.	Pursuing	Wireless Communication
2	M.Tech	2014	VLSI Systems Design
3	B.Tech	2009	Electronics & communication Engineering

2. Teaching and Learning:

2.1. Teaching Interests:

Basic Electronics , Analog and Digital electronics design, signals and systems , digital signal processing , Analog and digital communication , advanced digital signal processing for wireless communication,

2.2 Novel Teaching & Learning Techniques adopted:

Videos, PPT, Group Discussion and Quiz

3. Co-curricular and Extra-Curricular Activities

3.1. Interests and Hobbies: Reading books, listening Music, Playing kabaddi , badminton.

3.2. CCA/ECA Organized: Ymantra-2k14,

3.3. CCA/ECA participated: National youth day -2k16.

3.4. Counselling and Mentoring Activity: UG students

3.5. Committees involved in:

Department level: Sports and Academic Time tables.

Institute Level: NSS

4. Conference / Workshop / Seminar / Guest Lectures:

4.1. Conducted: Hands on training in VLSI design (MICROWIND AND DSCH2 Tools)

4.2. Attended:

(i) Recent Advances in Satellite and Radar Antennas (MTT/AP/EMC society, IEEE HYD)

(ii) Robotryst-2012

(iii) Embedded systems and RTOS

(iv) Learning Methodologies.

(v) Two-Week ISTE STTP "CMOS, Mixed Signal and Radio Frequency VLSI Design" conducted by IIT Kharagpur.

(vi) Two-Week ISTE STTP on Engineering Physics.

5. Academic Contribution and Research & Consultancy:

5.1. Invited Lectures: Nil

5.2. Articles/Chapters published in Books: Nil

5.3. Books published as single author or as editor: Nil

5.4. Projects Guided:

a) UG: 09

b) PG: 00

5.5. Research Interests: 5G and beyond Wireless communication, NOMA-MIMO, Short packet communication and URLLC.

5.6. Ph.D students: Nil

a) Enrolled:

b) Submitted:

c) Awarded:

5.7. Papers published in reviewed Journals:

S.No	Title of the Paper	Journal Name Vol.No. PP	ISBN/ISSN No.	Impact Factor/ Citation Index	National/ International
1	Low Power Multi-Bit Flip-Flops Design for VLSI Applications”	INTERNATIONAL JOURNAL OF ENGINEERING RESEARCH AND APPLICATIONS (IJERA)	ISSN 2248-9622	UGC	International

5.8. Papers presented at National / International conferences:

S.No	Title of the Paper	Names of the Conference/ Seminars	National/ International	Period
1	Low Power Multi-Bit Flip-Flops Design for VLSI Applications	NCDATES-2K15.	National	2015.
2	COMPACTION SCHEME FOR RAM USING ONLINE BIST	“EMERGING TRENDS IN ELECTRICAL & ELECTRONICS. ENGINEERING	International	2014,
	“ENHANCEMENT OF DIGITAL GABOR FILTER DESIGN USING VERILOG HDL	INTERNATIONAL CONFERENCE ON COMMUNICATION & SIGNAL PROCESSING (ICCS-2013),	International	2013.

5.9. Sponsored research Projects:

S.No	Title	Agency	Period	Grant amount	Ongoing / Completed

5.10 Consultancy Projects:

S.No	Title	Agency	Period	Sanctioned Amount	Ongoing / Completed
	Nil				

6. Awards / Honors received:

- Academic excellence
- Certificate of Appreciation for Academic Results.
- Best performance in Importance of mother tongue @ telugu sahitya academy.

7. Motto: You are what you believe in. You become that which you believe you can become.