

Name: **NAGALEELA SAIKAM**

Designation: Assistant Professor

Department: ECE

Mail ID: leela.saikam@gmail.com

Experience (in years): 8.4yrs Teaching: 8.4yrs Research: 0 Others (if any, specify):



1. Educational / Technical qualifications:

S.No	Level (UG / PG / Ph.D)	Year of passing	Specialization
1	M.Tech	2015	VLSI System Design
2	B.Tech	2008	Electronics and Communications
3	Secondary (10+2)	2004	MPC
4	10 th	2002	

1. Teaching and Learning:

- 2.1. Teaching Interests: Microprocessors, EDC, Communication, VLSI Design, Digital Logic Design, CPLD&FPGA
- 2.2. Novel Teaching & Learning Techniques adopted: WIT &WIL, LAB PROTOCOL, STORY BOARD
- 2.3. Involvement in curriculum updating / Design: NIL

3. Co-curricular and Extra-Curricular Activities

- 3.1. Interests and Hobbies: listening to music, gardening
- 3.2. CCA/ECA Organized: NIL
- 3.3. CCA/ECA participated:
 - Student Induction Program-2021
 - Industrial visit to INCOIS in June-2022
 - Industrial visit to NRSC, Balanagar in March-2022
- 3.4. Counseling and Mentoring Activity:
 - Mentoring 12 B.Tech students of 2021 batch
 - Mentoring 4 B.Tech students of 2022 batch
 - Class coordinator of ECE-1 of 2022 batch
- 3.5. Committees involved in:
 - Department level:
 - Summer Internship file incharge
 - Institute Level:
 - CDC Coordinator from ECE Department

4. Conference / Workshop / Seminar / Guest Lectures :

- 4.1. Conducted: NIL
- 4.2. Attended:
 1. Foundation Program in ICT For Education, Pedagogy For Online and Blended Teaching-Learning Process
 2. "Foundation Program in ICT for Education" conducted by IIT Bombay from 13th SEP to 18th OCT, 2018.
 3. One week FDP Program on "Embedded Systems & Interfacing Hands-on" jointly organised by Electronics and ICT Academies from June 10-14, 2019.
 4. One week FDP Program on VLSI Chip Design Hands-on using open source EDA" jointly organised by Electronics and ICT Academies from 8-

12th July,2019

1. Five-day online PDP on “NBA IN A NUTSHELL” organized by department of ECE, VNRVJIET held from 27th July-31st July,2020
2. One week workshop on “VNR Initiaves and Awareness on OBE” organized by department of CSE in collaboration with CSI VNR SC from 6th July-11th July,2020.
3. Two-week Online Faculty Development Programme on “Demystifying 5G RF ASICs” jointly organised by Electronics and ICT Academies held from 24 August to 04 September 2020.
4. Five day online National workshop on “Recent Trends in Microelectronic Devices,VLSI Circuits and their Applications” from 25th-29th Jan,2021 organized by MERG,ECE, Koneru Lakshmaiah Educational Foundation.
5. Two week Online FDP on System Design Methodologies for Embedded, IoT, AI, & HPC using Intel FPGA (19 - 30 April, 2021),conducted by NIT,Patna.
6. Two-week Online Faculty Development Programme on “Embedded UVM open-source Emulation & Functional Verification” jointly organised by Electronics and ICT Academies held from 13-24 July 2020
7. Participated in a Two-week ISTE STTP on “CMOS, Mixed Signal and Radio frequency VLSI Design” conducted by IIT Kharagpur from 30th Jan-4th Feb,2017.
8. Completed NPTEL Online Certification Course on “Principles of Communication systems-1” with Elite from Jan-Apr,2019.
9. Completed Coursera on “AI For Everyone” on Nov-26,2020.
10. Attended one week FDP on “Trends and Challenges In Advanced VLSI System Design” organized by Dept.of E.C.E, S.R.K.R Engineering College(A) during 21st – 26th February 2022.
11. Participated in SERB Sponsored International High-End Workshop on "Recent Trends in Semiconductor Devices/VLSI Chip Devices and its Sensor Applications", held from 23rd to 27th Jan, 2023 organized, by Microelectronics and VLSI Research Group (MERG), Department of ECE, Koneru Lakshmaiah Education Foundation (KL Deemed to be University), India.
12. Participated in FDP on “Design and Implementation of Advanced VLSI Architectures” organized by the Department of ECE, Lakireddy Bali Reddy College of Engineering, Mylavaram, AP, from 13th-17th March-2023
13. Participated in the Professional Development Programme on “Instructional Design and Delivery Systems” Conducted by NITTTR, Chennai from 20.03.2023 to 25.03.2023.

5. Academic Contribution and Research & Consultancy:

5.1. Invited Lectures:NIL

5.2. Articles/Chapterspublished inBooks:

5.3. Bookspublishedassingleauthororaseditor:

5.4. Projects Guided :

a) UG : 2

b) PG :

5.5. Research Interests : VLSI

5.6. Ph.Dstudents :

- a) Enrolled :
- b) Submitted:
- c) Awarded:

5.7. Papers published in reviewed journals :

S.No	Title of the Paper	Journal Name Vol.No. PP	ISBN/ISSN No.	Impact Factor/ Citation Index	National/ International
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5.8. Papers presented at National / International Conferences:

S.No	Title of the Paper	Names of the Conference/ Seminars	National/ International	Period
1	Low Leakage CNTFET Full Adders	IEEE Global Conference On Communication Technology(GCCT- 2015), April 23- 24,2015, Kanyakumari,India.	International	2014- 2015

5.9. Sponsored research Projects:

S.No	Title	Agency	Period	Grant amount	Ongoing / Completed

5.10 Consultancy Projects:

S.No	Title	Agency	Period	Sanctioned Amount	Ongoing / Completed

6.Awards / Honors received:Second best project in M.Tech

7.Motto: Work with commitment