

Name: **CH.Ganesh**

Designation: Assistant Professor

Department: Electronics & Communication Engineering

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Experience (in years): Teaching: 8.5yrs Research: 0 yrs others (Industry): 0 yrs



1. Educational / Technical qualifications:

S.No	Level (UG / PG / Ph.D)	Year of passing	Specialization
1	B.E.	2012	ECE
2	M. Tech	2014	VLSI Design
3	Ph.D	pursuing	VLSI Design

2. Teaching and Learning:

2.1. Teaching Interests: Circuit Theory, Network Analysis, Electronic Devices and Circuits, Pulse and Digital Circuits, Linear and Digital IC, DSD, DICA Electronic Circuit Analysis, EMI, EDA, DDTV, EMWTL, Memory Design, VLSI Design, FPGA, Mixed Signal and RF IC Design, Low power VLSI Design, Full Custom IC Design, Simulation and Synthesis with PLDs, Scripting Languages for VLSI, Analog Circuits

2.4. Novel Teaching & Learning Techniques adopted: POGIL, WIT AND WIL, Discussion Method, Brain storming, Mind mapping, Project based learning, PPTs, Videos, Group Discussions, Chalk and Talk etc. Subject Scenario

2.3. Involvement in curriculum updating / Design:

3. Co-curricular and Extra-Curricular Activities

3.1. Interests and Hobbies: Sports, Listening Music

3.2. CCA/ECA Organized:

3.3. CCA/ECA participated: Involved and helped students in

- Tech fests like project expo
- Freescale project Competitions

3.4. Counseling and Mentoring Activity: B.Tech Students.

3.5. Committees involved in:

Department level:

- NBA work -Student Achievements and Participations.
- Member of Project Review Committee for IV-ECE-II
- Member of Boards of Studies since 2018-19 academic year
- IEEE IACC 2017 Conference committee Member.

Institute Level:

- Member Disciplinary Committee at VNRVJiet.

4. Conference / Workshop / Seminar / Guest Lectures:

4.1. Conducted:

- Training program on “Low Power VLSI Design using CAD Tools” from 28th October to 02nd November 2019.
- Summer FDP on “VLSI Chip Design Hands on using Open Source EDA” from 8th -12th July 2019.
- A Three-Day “IACC 7th IEEE International Advance Computing Conference” – Registration Committee.
- A Two Day workshop on “FPGA Design Flow using VIVADO”.

4.2. Attended:

- A week FDP on “LaTeX & Technical Report Writing” from 25th to 30th May 2020.

- A Five Day FDP on “Establishing Research Beyond Horizon” from 26th to 30th May 2020
- 1 week workshop on “Sensor Networks and IOT” at E& ICT Academy NIT Warangal, at VNRVJIET, Hyderabad during 26th-31st Aug -2019
- Days workshop on "Designing with ZYNQ SOC and it's applications" at JNTU College of Engineering., Hyderabad during 16th-18th Oct,2019
- 1 Week workshop on "Python programming with Industry Perspective" at Electronics and ICT Academy, NIT Patna , and Dept. of ECE&EIE in Association with IEEE&ISTE, VNRVJIET, Hyderabad during 2nd-6th Dec,2019
- days workshop on “Technical Writing” at Dept. of H&S, VNRVJIET, Hyderabad during 28th-29th June-2019
- 1 Week workshop on “VLSI Chip Design Hands on using open source EDA” at E& ICT Academy IIT Guwahati at VNRVJIET, Hyderabad during 8th-12th July-2019
- FDP-NKN Summer Course-2019 on "Python programming with Industry Perspective" from 2nd to 6th December 2019.
- Summer FDP on “Embedded Systems & interfacing hands-on” 10th June 2019 to 14th June 2019.
- one week FDP on “ Adoption of FOSS for Effective Teaching & Learning in Engineering” held at CMR College of Engineering & Technology in collaboration with “Swecha” from 18th – 23rd June 2018.
- One week FDP on”VLSI Design Verification and Test” 26th to 30th May 2018, at NIT Warangal.
- One Week Workshop on “Integrated Circuit and System Design using CAD tools” 12th – 17th June’2017.
- A Two Day workshop on “FPGA Design Flow using VIVADO”.
- A Two Day National Workshop on “System design using XILINX FPGA” conducted by Department of electronics and communication, G.V.P.college of engineering Visakhapatnam, on 18th & 19th November 2011.
- Five Day Webinar on “Artificial Neural Network” conducted by NIT Faculty with the help of Electronics and Communication Department, RAGHU Engineering College, Visakhapatnam

5. Academic Contribution and Research & Consultancy:

5.1. Invited Lectures: Guest Lecture for Webinar series on “RTL Design and Verification” held from 9th June 2020 to 25th June 2020.

5.2. Articles/Chapters published in Books: Nil

5.3. Books published as single author or as editor: Nil

5.4. Projects Guided:

a) UG: 15

b) PG: 4

5.5. Research Interests:

VLSI design

5.6. Ph.D students:

a) Enrolled:

b) Submitted:

c) Awarded:

5.7. Papers published in reviewed Journals:

S.No	Title of the Paper	Journal Name Vol.No. PP	ISBN/ISSN No.	Impact Factor/ Citation Index	National/ International
1	Thermal Stability Analysis of Single-Ended 9T-ST Robust SRAM Cell for Near-Threshold Operation	International Journal of Nanoscience	Vol. 20, No. 6 (2021) 2150057	1.3	International
2	Design and Implementation of Dual Mode Compressor Based 32 Bit Dadda Multiplier using Modified Carry Select Adder	International Journal of Innovative Technology and Exploring Engineering	2278-3075	0.54	International
3	Efficient driver design for AMOLED displays	ARNP Journal of Engineering and Applied Sciences	1819-6608	0.75	International
4	Dual-threshold Single-ended Schmitt-Trigger Based Radiation Hardened memory Design with Fault modeling System	IOSR Journal of VLSI and Signal Processing (IOSR-JVSP)	2319 – 4200	2.82	International
5	Novel Architecture of UTMI Module in USB	International Journal & Magazine of Engineering, Technology, Management and Research	2348-4845	3.55	International

5.8. Papers presented at National / International conferences:

S.No	Title of the Paper	Names of the Conference/ Seminars	National/ International	Period
1	Design and Analysis of 8T SRAM with Assist Schemes (UDVS) In 45nm CMOS	2017 International Conference on circuits Power and Computing Technologies [ICCPCT]	International	2017
2	High Speed ASIC Implementation of Pipelined DDR SDRAM Controller	IEEE International Conference on	International	2014

		Information Communication & Embedded System "ICICES 2014"		
3	Design of Low Power and High Speed Modified Carry Select Adder for 16 bit Vedic Multiplier	IEEE International Conference on Information Communication & Embedded System "ICICES 2014"	International	2014

5.9. Sponsored research Projects:Nil

S.No	Title	Agency	Period	Grant amount	Ongoing / Completed

5.10 Consultancy Projects:Nil

S.No	Title	Agency	Period	Sanctioned Amount	Ongoing / Completed

6. Awards / Honors received: Reviewer for IEEE Transactions on Very Large Scale Integration Systems

7. Motto: Work is Worship & Efficiency is Divine