



Estd.1995

**VALLURUPALLI NAGESWARA RAO VIGNANA JYOTHI  
INSTITUTE OF ENGINEERING AND TECHNOLOGY**  
An Autonomous Institute, NAAC Accredited with 'A<sup>++</sup>' Grade  
NBA Accredited for CE, EEE, ME, ECE, CSE, EIE, IT B.Tech Courses  
Approved by AICTE, New Delhi, Affiliated to JNTUH  
Recognized as "College with Potential for Excellence" by UGC  
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## DEPT OF ECE

<b>Consolidated guest lectures</b>		
<b>S. No</b>	<b>Years</b>	<b>Number of Guest Lectures organised</b>
1	2020-2021	14
2	2019-2020	41
3	2018-2019	9
4	2017-2018	15
5	2016-2017	13
6	2015-2016	20
7	2014-2015	9

## Guest lectures



### VALLURUPALLI NAGESWARA RAO VIGNANA JYOTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

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## Guest lectures 2020-21

S. No.	Name & Details of the person who delivered the lecture	Topic of the Guest Lecture	Dates of the Lecture	For whose benefit the lecture was organized	No. Attended	Impact of the lecture
1.	Mr.Srikar Arepalli(Alumni student 2010 passed out)	Introduction to FOSS and Basic Linux Commands	6 <sup>th</sup> January 2021	II B.Tech ECE-1 & 2 students	120	It will help students gain philosophical understanding of how FOSS evolved, what is it's significance today, how to install a GNU/Linux distribution, and usage of basic Linux commands. The session will be interactive and interesting as they experience freedom in technology.
2.	Mr.Ch.Ganesh-Asst.Prof	Amplifier Design	7 <sup>th</sup> January 2021	II B.Tech ECE-2 students	62	<ul style="list-style-type: none"> <li>• Design of biasing circuit to make the transistor operate in the active region</li> <li>• Stability calculation of each biasing circuit using the stability factor.</li> </ul>

### Guest lectures

S. No.	Name & Details of the person who delivered the lecture	Topic of the Guest Lecture	Dates of the Lecture	For whose benefit the lecture was organized	No. Attended	Impact of the lecture
						<ul style="list-style-type: none"> <li>• Compensation techniques to design stable Amplifier.</li> </ul>
3.	Sri.D.Srikanth, Engineer , Xilinx, Hyderabad	Hands on session UNIX Platform	20 <sup>th</sup> & 27 <sup>th</sup> January 2021	I & II M.Tech students	35	Aimed to train the participants in Vi tool of synopsis. Practice session is planned to work with I tools. Target audience for this event is PG(VLSI) students and faculty working in VLSI area.
4.	Ms.Jhansi-Asst.Prof,CSE,VNRVJIET	Basics of C-Concepts	23 <sup>rd</sup> March 2021	I B.Tech ECE-C	55	It will help students to recall all the basics related to the Programming though C which will be revision and fetch the students to gain good marks in the End examinations
5.	Bashakarla Karthik(16071A0410) & Yellenki Ritheesh Baradwaj(16071A0460)	Coding skills	02-01-2020	II B.Tech ECE-1,3 & 4	200	Discussed for the following points. <ul style="list-style-type: none"> <li>• Coding skills</li> <li>• GITHUB account</li> <li>• Projects</li> <li>• Design Thinking and its importance</li> <li>• Mentoring</li> </ul>
6.	Akash Moses (16071A0401)	Coding skills	04-01-2019	III B.Tech ECE-2 students	60	Discussed for the following points. <ul style="list-style-type: none"> <li>• Coding skills</li> <li>• GITHUB account</li> <li>• Projects</li> <li>• Design Thinking and its importance</li> <li>• Mentoring</li> </ul>
7.	Mr.Jaya Deva Rao	Motivation and Goal Setti	6 <sup>th</sup> January, 2020	II B.Tech ECE students	260	He explained the following points. <ul style="list-style-type: none"> <li>• Don't neglect small things in Daily life.</li> <li>• Small things are beautiful and they make a large difference.</li> </ul>

### Guest lectures

S. No.	Name & Details of the person who delivered the lecture	Topic of the Guest Lecture	Dates of the Lecture	For whose benefit the lecture was organized	No. Attended	Impact of the lecture
						<ul style="list-style-type: none"> <li>• Have a habit of forgiving others Assign your goal, design your dream and don't stop till you get enough.</li> <li>• Initially enhance what you are capable of, everyone is good at something, not everyone does everything</li> </ul>
8.	Mr.A.Ramesh Kumar-Assoc.Prof	Overview of Microprocessor & Microcontrollers	07-01-2020	III B.Tech ECE-2	55	He focussed on microprocessor architecture, microcontrollers and real time applications microprocessors and its features, instruction sequence and execution. Based on addressing modes, the instruction set will be designed the instruction set will be designed. The modes of operation of 8086 microprocessors also discussed. The role of processor in real time applications and programing behaviour discussed.
9.	Dr.V.Padmaja	Overview of Microprocessor & Microcontrollers	08-01-2020 & 10-01-2020	III B.Tech ECE-2	55	She focussed on microprocessor architecture, microcontrollers and real time applications microprocessors and its features, instruction sequence and execution. Based on addressing modes, the instruction set will be designed the instruction set will be designed. The modes of operation of 8086 microprocessors also discussed. The role of processor in real time applications and programing behaviour discussed.
10.	Mr.G.Radha Krishna	Electronic Measurements and Instrumentation	08-01-2020	III B.Tech ECE-2	55	He explained the following points <ul style="list-style-type: none"> <li>• Hybrid Technologies</li> <li>• Multi range meters</li> <li>• ECM &amp; ECCM</li> <li>• EMI/EMC</li> </ul>
11.	A.Srinivasa Reddy	Applications of Science and Technology	20-01-2020	I B.Tech ECE	30	He discussed about this technology is the application of science to the making of artefacts is a widely-held, persistent and

### Guest lectures

S. No.	Name & Details of the person who delivered the lecture	Topic of the Guest Lecture	Dates of the Lecture	For whose benefit the lecture was organized	No. Attended	Impact of the lecture
						<p>influential view. Considerable scholarly work has been done during the past quarter century to refute it on the grounds that it is historically and ontologically inaccurate. It is a view which fails to recognise the contribution of non-scientific factors to technological development, which neglects the reverse contribution of technology to science, and which offers a superficial account of the process of application. This paper focusses on this last point, and argues that in those cases where science is applied to technology, the application process is usually exceedingly complex. The process involves the selection of appropriate knowledge, the adoption of differing criteria and the translation and re-shaping of knowledge to make it amenable to the technologist. The issue has important implications for the school curriculum.</p>
12.	Ms.K.B.Archana	Over view of Communication	13-02-2020	II B.Tech ECE-1	50	<p>The guest lecture was aimed to provide students with the applications of Electronics Communications using both Analog and Digital Communications. As of current trends in electronics communication being digital in nature, the content was more focused on digital communications covering aspects of A/D conversion, Digital Modulations, Cryptography etc. An application of Digital communications in wireless communications was discussed with challenges of the medium and its solutions. Different forms of wireless communications such as WI-FI, Cellular, Satellite, IOT connectivity standards were also discussed</p>

### Guest lectures

S. No.	Name & Details of the person who delivered the lecture	Topic of the Guest Lecture	Dates of the Lecture	For whose benefit the lecture was organized	No. Attended	Impact of the lecture
13.	Ms.N.Dhana Lakshmi	Over view of Digital System Design	13-02-2020	II B.Tech ECE-1 students	50	<ul style="list-style-type: none"> <li>➤ To enhance the knowledge towards the design of digital systems for real-time applications.</li> <li>➤ To discuss how logic design transforms algorithms and processes conceived by people into computing machines.To explain the possibility of designs simulated in software, tested on a field-programmable gate array (FPGAs) and also implemented using breadboards</li> </ul>
14.	D.Yadagiri. Design Verification Engineer, AMD	Bridging gap between Industry and Academia	29-08-2020	M.Tech II Year VLSI and ES students	20	<p>Students Explore to education not only makes students better citizens of the world by inculcating values and good habits, but it also helps students to be technically sound so that they can compete with the outside world.</p> <p>Students learnt what are the approaches need to be taken to meet the expectations of the Industry as a fresher</p>



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## DEPT OF ECE

### Details of Guest Lectures Organized from January, 2020 to June, 2020

S.No.	Name & Details of the person who delivered the lecture	Topic of the Guest Lecture	Dates of the Lecture	For whose benefit the lecture was organized	No. Attended	Impact of the lecture
1	Bashakarla Karthik (16071A0410) & Yellenki Ritheesh Baradwaj (16071A0460)	Coding skills	02-01-2020	III B.Tech ECE-1, 3 & 4 students	200	Discussed for the following points. <ul style="list-style-type: none"><li>• Coding skills</li><li>• GITHUB account</li><li>• Projects</li><li>• Design Thinking and its importance</li><li>• Mentoring</li></ul>
2	Akash Moses (16071A0401)	Coding skills	04-01-2020	III B.Tech ECE-2 students	60	Discussed for the following points. <ul style="list-style-type: none"><li>• Coding skills</li><li>• GITHUB account</li><li>• Projects</li><li>• Design Thinking and its importance</li><li>• Mentoring</li></ul>

3	Mr.Jaya Deva Rao	Motivation and Goal Setti	6 <sup>th</sup> January, 2020	II B.Tech ECE students	260	<p>He explained the following points.</p> <ul style="list-style-type: none"> <li>• Don't neglect small things in Daily life.</li> <li>• Small things are beautiful and they make a large difference.</li> <li>• Have a habit of forgiving others Assign your goal, design your dreamand don't stop till you get enough.</li> <li>• Initially enhance what you are capable of, everyone is good at something,not everyone does everything</li> </ul>
4	Mr.A.Ramesh Kumar- Assoc.Prof	Overview of Microprocessor & Microcontrollers	07-01-2020	III B.Tech ECE-2 students	55	<p>He focussed on microprocessor architecture, microcontrollers and real time applications microprocessors and its features, instruction sequence and execution. Based on addressing modes, the instruction set will be designed the instruction set will be designed. The modes of operation of 8086 microprocessors also discussed. The role of processor in real time applications and programing behaviour discussed.</p>
5	Dr.V.Padmaja	Overview of Microprocessor & Microcontrollers	08-01-2020 & 10-01-2020	III B.Tech ECE-2 students	55	<p>She focussed on microprocessor architecture, microcontrollers and real time applications microprocessors and its features, instruction sequence and execution. Based on addressing modes, the instruction set will be designed the instruction set will be designed. The modes of operation of 8086 microprocessors also discussed. The role of processor in real time applications and programing behaviour discussed.</p>
6	Mr.G.Radha Krishna	Electronic Measurements and Instrumentation	08-01-2020	III B.Tech ECE-2 students	55	<p>He explained the following points</p> <ul style="list-style-type: none"> <li>• Hybrid Technologies</li> <li>• Multi range meters</li> <li>• ECM &amp; ECCM</li> <li>• EMI/EMC</li> </ul>



7	A.Srinivasa Reddy	Applications of Science and Technology	20-01-2020	I B.Tech ECE students	30	He discussed about this technology is the application of science to the making of artefacts is a widely-held, persistent and influential view. Considerable scholarly work has been done during the past quarter century to refute it on the grounds that it is historically and ontologically inaccurate. It is a view which fails to recognise the contribution of non-scientific factors to technological development, which neglects the reverse contribution of technology to science, and which offers a superficial account of the process of application. This paper focusses on this last point, and argues that in those cases where science is applied to technology, the application process is usually exceedingly complex. The process involves the selection of appropriate knowledge, the adoption of differing criteria and the translation and re-shaping of knowledge to make it amenable to the technologist. The issue has important implications for the school curriculum.
8	Ms.K.B.Archana	Over view of Communication	13-02-2020	II B.Tech ECE-1 students	50	The guest lecture was aimed to provide students with the applications of Electronics Communications using both Analog and Digital Communications. As of current trends in electronics communication being digital in nature, the content was more focused on digital communications covering aspects of A/D conversion, Digital Modulations, Cryptography etc. An application of Digital communications in wireless communications was discussed with challenges of the medium and its solutions. Different forms of wireless communications such as WI-FI, Cellular, Satellite, IOT connectivity standards were also discussed

9	Ms.N.Dhana Lakshmi	Over view of Digital System Design	13-02-2020	II B.Tech ECE-1 students	50	<ul style="list-style-type: none"> <li>To enhance the knowledge towards the design of digital systems for real-time applications.</li> <li>To discuss how logic design transforms algorithms and processes conceived by people into computing machines.To explain the possibility of designs simulated in software, tested on a field-programmable gate array (FPGAs) and also implemented using breadboards</li> </ul>
11	K B Archana, Sr. Asst. Professor ECE dept & B Shabarinath, Asst. Professor ECE dept	IOT programming	12-10-2019	III B.Tech ECE students	100	<p>Explained following points.</p> <p>With introduction to Python programming followed with basics of Python commands, Libraries, Functions and developing a Calculator in python</p>
12	JLV Ramana Kumari-Asst.Prof , K.Naresh-Asst.Prof & Ch.Ganesh- Asst.Prof	Vivado Design flow and applications Using ZYNQ SoC	26-10-2019	Faculty	6	<p>The lecture includes:</p> <ul style="list-style-type: none"> <li>Xilinx FPGA's Architecture, Vivado design flow, Use Vivado IDE to create a sequential HDL code to design a frequency divider/2-bit counter Generate the bit-stream and verify in hardware.</li> <li>Lab : Create an Application and Implementing a Hardware Debugging and VIO using the IP Core and Writing the Verilog code for own application.</li> </ul>
13	Mr.Yadagiri, Project Engineer, AMD	Motivating the students towards the Carrier in VLSI Industry	23-11-2019	I M.Tech students	25	<p>He explained workflow of AMD. How to get a internship or job in AMD. How to prepare written test .What are the subjects the students have to concentrate for both VLSI and ES students. Aptitude preparation , what are software tools the students have to practice etc. are discussed.</p>
14	Manjunath Chandrashekar-Project Manager Academic Alliances, BluePrism	online learning platform	20-12-2019	II B.Tech ECE-3 students	50	<p>He explained the process to register the training and certificate courses in blue prism university. Blue Prism University is an online learning platform, available to all, that supports self-study, on-demand and in-person training on Robotic process automation basics.</p>

15	Mr. Sanaulla, a soft Skills Trainer	Soft Skills and Spoken English	17-12-2019	III B.Tech ECE-3	50	<p>He explained following points</p> <ol style="list-style-type: none"> <li>1) How to get fluency in English?</li> <li>2) How to improve English vocabulary?</li> <li>3) Spotting Errors.</li> <li>4) Conversation Practice.</li> </ol> <p>Soft Skills:</p> <ol style="list-style-type: none"> <li>1) Personality Development.</li> <li>2) Facial Expressions.</li> <li>3) Body Language.</li> </ol>
16			18-12-2019	III B.Tech ECE-4	52	
17	Mr. Sanaulla, a soft Skills Trainer	Soft Skills and Spoken English	19-12-2019	II B.Tech ECE-2	50	
18				III B.Tech ECE-1	52	
19				II B.Tech ECE-4	51	
			20-12-2019	II B.Tech ECE-3	50	
20	Prof. Gregory Collier and Mr.Kish Nangegadda	Entrepreneurship and opportunities for Entrepreneurs	18-12-2019	III B.Tech students	150	The session was coordinately by the Global Relationships office and the Entrepreneurship Development cell at the institute

## Details of Guest Lectures Organized from October, 2019 to December, 2019

S. No.	Name & Details of the person who delivered the lecture	Topic of the Guest Lecture	Dates of the Lecture	For whose benefit the lecture was organized	No. Attended	Impact of the lecture
1	K B Archana, Sr. Asst. Professor ECE dept & B Shabarinath, Asst. Professor ECE dept	IOT programming	12-10-2019	III B.Tech students	100	Explained following points. With introduction to Python programming followed with basics of Python commands, Libraries, Functions and developing a Calculator in python
2	JLV Ramana Kumari-Asst.Prof , K.Naresh- Asst.Prof & Ch.Ganesh- Asst.Prof	Vivado Design flow and applications Using ZYNQ SoC	26-10-2019	Faculty	6	The lecture includes: <ul style="list-style-type: none"> <li>• Xilinx FPGA's Architecture, Vivado design flow, Use Vivado IDE to create a sequential HDL code to design a frequency divider/2-bit counter Generate the bit-stream and verify in hardware.</li> <li>• Lab : Create an Application and Implementing a Hardware Debugging and VIO using the IP Core and Writing the Verilog code for own application.</li> </ul>
3	Mr.Yadagiri, Project Engineer, AMD	Motivating the students towards the Carrier in VLSI Industry	23-11-2019	I M.Tech students	25	He explained workflow of AMD. How to get a internship or job in AMD. How to prepare written test .What are the subjects the students have to concentrate for both VLSI and ES students. Aptitude preparation , what are software tools the students have to practice etc. are discussed.
4	Manjunath Chandrashekar- Project Manager Academic Alliances, BluePrism	online learning platform	20-12-2019	II B.Tech ECE-3	50	He explained the process to register the training and certificate courses in blue prism university. Blue Prism University is an online learning platform, available to all, that supports self-study, on-demand and in-person training on Robotic process automation basics.

5	Mr. Sanaula, a soft Skills Trainer	Soft Skills and Spoken English	17-12-2019	III B.Tech ECE-3	50	<p>He explained following points</p> <ol style="list-style-type: none"> <li>1) How to get fluency in English?</li> <li>2) How to improve English vocabulary?</li> <li>3) Spotting Errors.</li> <li>4) Conversation Practice.</li> </ol> <p>Soft Skills:</p> <ol style="list-style-type: none"> <li>1) Personality Development.</li> <li>2) Facial Expressions.</li> <li>3) Body Language.</li> </ol>
				III B.Tech ECE-4	52	
6	Mr. Sanaula, a soft Skills Trainer	Soft Skills and Spoken English	18-12-2019	II B.Tech ECE-2	50	
				III B.Tech ECE-1	52	
7	Mr. Sanaula, a soft Skills Trainer	Soft Skills and Spoken English	19-12-2019	III B.Tech ECE-2	50	
				II B.Tech ECE-4	51	
8	Mr. Sanaula, a soft Skills Trainer	Soft Skills and Spoken English	20-12-2019	II B.Tech ECE-3	50	
9	Prof. Gregory Collier and Mr.Kish Nangegadda	Entrepreneurship and opportunities for Entrepreneurs	18-12-2019	III B.Tech students	150	

## Details of Guest Lectures Organized from April, 2019 to September , 2019

S. No.	Name & Details of the person who delivered the lecture	Topic of the Guest Lecture	Dates of the Lecture	For whose benefit the lecture was organized	No. Attended	Impact of the lecture
1	Mr.A.Srikhar (Alumni Student), Software Engineer, Accenture, Hyderabad	Open Sources and Free Software	12-04-2019	I B.Tech ECE-2 students	46	<p>In this session he gave introduction about free software, advantages of free software. He discussed about Linux and its role in making GNU. He explained about Richard Stallman and his free software movement. This session was very inspiring that feedback was given by students.</p> <p>The advantages of open source resolution are</p> <ul style="list-style-type: none"> <li>• Privacy</li> <li>• Editing &amp; modification of the code</li> <li>• Distribution</li> </ul> <p>He also explained about ULA, UNIX, GNU, GNU LINUX, GPL, VLL etc. The session was very interesting feedback given by students continue the sessions regularly. Students know about propitiatory softwares what will be effect if software is open , it is patented. Students learn what will happen if software is hidden and how it will effect our privacy is also known. Students came to know about Stallman who started this in 1983 and how companies opposed him and how he was able to make people know his idea.</p>
			13-04-2019	I B.Tech ECE-1 & ECE-4	90	

2	Prof. Sriram Jayasimha	Education: Tools that Empower	29-04-2019	Faculty	30	He stressed the need to empower greater number of students. He noted that in outcome -based education, many outcomes are possible, and the correct one should be identified. He noted that subject is more important than the person who delivers it. The teacher should showcase successful things, able to do quick experiments, and focus on important things. Noting that surrounding eco system of professional excellence, helps in the development of an institution, the limitations of geography must be overcome.
3	Mr. Madusudhana Reddy, verification Engineer, Aricent, Hyderabad	VLSI Design verification using System Verilog	27.04.2019	M.Tech Students& Faculty	30	He discussed the following points <ul style="list-style-type: none"> <li>• Need of verification in VLSI design</li> <li>• Basic syntax of System Verilog</li> <li>• System Verilog Test Bench design obtaining code coverage for a digital design in System Verilog</li> <li>• Use of the randomization, Constraint Randomization in verifying the design using test bench</li> <li>• Use of Assertions in Verifying the behavior of the System</li> </ul>
4	Dr. Shravan Kumar Bandari, postdoc from Dept. of Electrical & Computer Engineering, SNU, Seoul, South Korea	w i r e l e s s communication	09-07-2019	III B.Tech ECE-1&3	110	He suggested some topics on communication for students to do good academic projects and publish their work in reputed conferences and journals. He also discussed his research work to motivate them.
5	Mr.A.Srikar (Alumni Student), Software Engineer, Accenture, Hyderabad	Open Sources and Free Software	11-07-2019	III B.Tech ECE-4	60	In this session he gave introduction about free software, advantages of free software. He discussed about Linux and its role in making GNU. He explained about Richard Stallman and his free software movement. This session was very inspiring that feedback was given by students.

6	Mrs.G.Sahithya- Asst.Prof & K.B.Archana- Sr.Asst.Prof	Over view of Communications	15-07-2019	III B.Tech students	70	They gave brief introduction about what is communication, analog communication, digital communications and cellular communications. What is importance of the digital and they discussed about latest trends wireless communications. And they explained about 2G, 3G,4G and 5G and applications of 4G & 5G.
7	K.B.Archana- Sr.Asst.Prof on	Over view of D i g i t a l Communications	18-07-2019	ECE-1 students	70	Several blocks comprise the transmitter and receiver sections of Digital communications which may be mandatory or optional. Some of the blocks discussed are source encoding block, compression block, encryption block, channel coding and Digital modulation blocks.
8	Dr. L Anjaneyulu, Professor, ECE , NITW, Warangal	Over view of C o m p u t e r Networks	26 <sup>th</sup> July, 2019	III B.Tech ECE-1 & 2	120	Discussed about different types of networks, Layered architecture and different protocols. Discussed about practical orientation of IP addresses and different opportunities In networking area. And also discussed about research problems.
9	Mr. K Rama Krishna Reddy, SMTS Design Engineer, AMD India Pvt Ltd, Bangalore, Alumni of our college	C P U Benchmarking	31-08-2019	III B.Tech. EIE & ECE students	65	The session mainly focused on the new technologies in the computer architecture industry and what are the different tests carried out to performance of CPU's of different manufactures and types. A typical CPU benchmark test will compare the test system against the standards for the type of CPU used. CPU specifications typically measured by a benchmark test includes the clock speed, the number of instructions executed, registry calls per cycle and overall architecture efficiency factors. The benchmark standards change between generations of CPUs and also between Intel and AMD CPUs.CPU Benchmark results ("Baselines") were gathered from users' submissions to the Pass Mark web sit and from internal testing. Performance Test conducts eight different tests and then averages the results together to determine the CPU Mark for a system.



10	Dr Vijender B Reddy, Sc-SD, ADRIN dept of space, Hyderabad	Security and Privacy in IoT Systems	31 <sup>th</sup> August 2019	students and Faculty	50	In this Guest Lecture, participants were asked to come with, reflect and comment on concrete minimum baseline security and privacy principles to create a trusted IoT environment. The main purpose of the lecture is to discuss minimum baseline security and privacy requirements along the entire networked architecture and value chain similar in various sectors. The outcome of the lecture should contribute to cross-sectorial top 5 to 10 main baseline principles per IoT domain addressed during this Guest Lecture, being.
11	K. Sai Deep-Developer System Analytics & Thakur Nandaraj Singh-Sr.Software Engineer	Introduction of Data science and data analytics	09-09-2019	III B.Tech ECE students	185	He explained the following topics <ul style="list-style-type: none"> <li>• Statistics for Data Science.</li> <li>• Foundations of Probability</li> <li>• Introduction to Data Modeling.</li> <li>• Supervised Machine Learning Methods.</li> <li>• Unsupervised Machine Learning Methods</li> <li>• Deriving Business Rules from Data</li> </ul>
12	Mr.Ravi, Monitra Heathcare Pvt Ltd, Hyderabad	Importance of IoT and its applications in health care	24-09-2019	II , III B.Tech and I M.Tech (VLSI & ES) students	300	Good



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<b>2018-19</b>						
S.No	Name of the Resource Person	Designation	Organization	Topic	Venue	Date
1	Mr.A.Sai Kumar , Mr.Arjun Varma & Mr.E.Shashikant	Alumni students	Alumni students	Importance of studies	B-213	21-07-2018.
2	Dr.SUDHARSHAN NELATURY	Professor, Electrical Engineering	Penn state University ERIE Campus	ANTENNA AND RADIATION THEORY	C-307	09-07-2018 to 10-07-2018
3	Mr.Ravi Shankar, Ms.Mrudhika & Mrs.Sravanthi	Research Associates	IIIT, Hyderabad	virtual labs training program	C-208	21-08-2018
4	Mr.Mahesh Gummaraju,	Head-Campus Learning Solutions	UTL Technologies Ltd, Bangalore	Embedded Systems	C-307	09-08-2018
5	Ms.K.Swetha	Software Engineer	TCS,Hyderabad	Data Analytics & Career Opportunities	B-Block Seminar Hall	03-08-2018
6	Dr.P.Srihari	Profrrsor	VNRVJIET,Hyderabad	Probability	B-212	26-07-2018

7	Mr.Francis David,	Associate Operations,	Luvieen Alva,	CEO on "Mentor Mind"	B-Block Seminar Hall	21-01-2019
8	Sri. Mahesh.U Patil	Joint Director	,C-DAC, Hyderabad	Overview of Microprocessors	B-Block Seminar Hall	21-01-2019
9	Mr.Vijaya Kumar Bhashamalla	Sr.Engineer	BOSCH, Delhi Gurgaon	Need for self-learning	B-213 & 214	18-01-2019 to 19-01-2019
<b>2017-18</b>						
1	Dr. D. Sita Rama Raju	Scientist(Retd.)	ADA	Tejas Indian fighter aircraft-Flight control and Avionics	Dr. A.P.J.AbdulKalam Auditorium	8th August 2017
2	Dr.P.Sri Hari	Professor	VNRVJIET	Over View of Digital Communication	C-213	10th August 2017
3	G. Muralidhar	ITS Principal advisor	Telecom Regulatory Authority of India (TRAI),	Telecommunication trends in ICT and 5G and Regulatory framework In India	C-213	23th September 2017
4	Pankaj S. Dhakate	Head of Signalling and Train control system, Automatic Fare Collection System	L& T Metro Rail Hyderabad	Communication Based Train Control for Hyderabad Metro Rail	Dr. A.P.J.AbdulKalam Auditorium	29th December 2017

5	Dr.L.Anjaneyulu	Professor ECE	NIT Warangal	Mobile Architecture beyond 3G	B-block Seminar hall	29th December 2017
6	Dr.Tummalla Ram Mohan Lal	Professor	IIT Hyderabad	Interactive session	B-block Seminar hall	9th January 2018
7	Dr.P.Srihari	Professor	VNR VJIET	Analog Communications and its importance	B-block Seminar hall	19th February 2018
8	Dr.P.Srihari	Professor	VNR VJIET	random variables in Machine Learning	B-block Seminar hall	19th February 2018
9	G.Nagaraju	Assistant Professor	VNR VJIET	Perceptron	C - 208	22nd February 2018
10	Dr.NishanthDongari	Professor	IIT Hyderabad	How to Write good Research Proposals	B-block Seminar hall	23rd February 2018
11	Dr.BharatKarmuri	Professor	USA	Study of Sudden Unexpected Death in Epilepsy using Machine Learning and Association Measures	P103	31th March 2018

12	Mr.M.Vijay Kumar	CEO	I-WIN IP Services Pvt. Ltd	The Importance of Intellectual Property Rights(IPR) in Business	B'Block Seminar Hall	31st March, 2018
13	Dr.S.Nagini	Associate Professor	VNRVJIET	Overview of Data Structures	B'Block Seminar Hall	9th April 2018
14	V.Baby	Associate Professor	VNRVJIET	Overview of Data Structures	B'Block Seminar Hall	9th April 2018
15	Ms.Indira	Assistant Professor	VNRVJIET	fundamentals of Python and programming	C - 208	22nd, 29th March and 4th , 11th April 2018
<b>2016-17</b>						
1	B.Suresh Daniel and P.Suman	Manager	M/s Logiquest Kaplan, Begumpet	GRE Orientation Talk	C - 208	8 <sup>th</sup> September 2016
2	Mr.Bharath	G.C	i3 – INFLUENCE INSPIRE IMPACT	Inspirational Lecture	K.S. Auditorium	8 <sup>th</sup> and 9 <sup>th</sup> September 2016

3	Pankaj Dhakate	General ManagerHead- Signaling and AFC,,	L & T Metro Rail	Communication based signaling in Metro Rail	C - 208	10 <sup>th</sup> September 2016
4	Mr. Sai	Alumni Student	ECE Department	Big Data and Data Science	K.S. Auditorium	17 <sup>th</sup> October 2016
5	Mr.RaviBoonapalli,	Manager, Business Development	HEP, Mentor Graphics	Awareness on CAD Tools	B-block Seminar Hall	17 <sup>th</sup> October 2016
6	Mr.clifkussmaul	associate professor	computer science, muhlenberg college	Minutes of Meeting	C - 208	19 <sup>th</sup> and 20 <sup>th</sup> December 2016
7	Krishnasraya Das and SrikantKesar Das	Priest	ISKCON Kukatpally, Hyderabad	Science of Self Realization	B-Block Seminar hall	21 <sup>th</sup> January 2017
8	PadmasriDr.M. Y.S.Prasad	Former Director	Satish Dhawan Space Center SHAR,ISRO	Space Communication	K.S auditorium.	23 <sup>rd</sup> January 2017

9	Sri. Mahesh U Patel	Principal Technical Officer	C-DAC,	Applications of Processors and Controllers	B-block Seminar Hall	25 <sup>th</sup> March 2017
10	Sri. Lakshmi	Scientist - F	DRDO.	DRDO products and Technologies	K.S. Auditorium	25 <sup>th</sup> March 2017
11	Mrs. HemaLatha	Assistant Professor	Dept.of.CSE, JNTUCEH, Hyderabad	Data Structures- Graph Theory	K.S. Auditorium	31 <sup>th</sup> March 2017
12	Sri. Ravi Shanker	Software Engineer	IT Solutions, Hyderabad	Java	C - 208	31 <sup>th</sup> March 2017
13	Don Hamilton	Senior Systems Consultant, EMEA	KEYSIGHT Technologies, Wokingham, Berks, United Kingdom	Importance of DSP	C - 208	7 <sup>th</sup> April 2017

**2015-16**

1	Abhimanyu Pamulapati	Co-founder &CTO	samosa labs software solutions Pvt. Ltd	Java programming – exploring IO package and net package	B-block Seminar Hall	30-03-2016 & 29-03-2016
2	Sri. Anand S Moghe	Senior Manager	Xilinx, Hyderabad.	Design for testability	K.S auditorium.	19-03-2016
3	Mr. Chalapathi	Assoc.Professo r	ECE, MVSR college of engineering.	Fundamentals of circuit theory	B-block Seminar Hall	19-02-2016
4	Sri. Teja	Application Engineer	Smart Bridge Educational Services PVT LTD	IOT applications and development	K.S auditorium	16-02-2016
5	Ahmed Nazeer	Business manager, CEO	VSES India PVT LTD	Application of IOT in various business sectors	B-block Seminar Hall	13-02-2016
6	Sri. AmarenderKatkam,	CEO	VSES India PVT LTD	Internet of things	B-block Seminar Hall	12-02-2016



7	Sri. K. V. Chandra Shekar,	Head for global data processing division	NRSC, Hyderabad	Remote sensing and overview of Digital Image Processing and its programming	K.S auditorium	23-01-2016
8	N.Madhusudha Reddy	Design Engineer	CYIENT, Hyderabad	HDL Based verification using System Verilog	B-block Seminar Hall	23-01-2016
9	MunamalaSyamala,	Associate Trainer	Indo global services	Concurrent PCB Design	K.S auditorium	19-01-2016
10	Challa Rami Reddy	JE	sigma solutions, Hyderabad	Chip to package PCB Layout	B-block Seminar Hall	18-01-2016
11	Priyanka anuganti,	JE	Techno trans	PCB Design using Keycad Software	B-block Seminar Hall	13-01-2016
12	Raavi Santosh Kumar,	Technical Associate	PrayoglabsPvt Ltd	PCB etching, Drilling and soldering	B-block Seminar Hall	12-01-2016
13	Mrs. G. Sreelatha	Application Engineer	Campus plus institute.	Industry Employability skills training	B-block Seminar Hall	21-12-2015

14	N.Madhusudha Reddy	Design Engineer	CYIENT, Hyderabad	System Verilog	B-block Seminar Hall	15-12-2015
15	Sri. Anand S Moghe	Senior Manager	Xilinx, Hyderabad	HDL based Design and verification using verilog	K.S auditorium	11-12-2015
16	Sri. K .Vijay Kumar Gupta	MD & CEO	kwalitphotonics Pvt.Ltd, kushaiguda, Hyderabad	LEDs in Real Time Applications	K.S auditorium	14-10-2015
17	Sri Madhav Negi	Associate Director	Global PMO, CSC	Effective leadership	B-block Seminar Hall	06-10-2015
18	Dr. L. Anjaneyulu	Associate Professor	NIT Warangal	Technologies of 3G and Beyond	B-block Seminar Hall	21-09-2015
19	Shiban K Koul,Deputy	director(S &P) and professor center for applied Researchh in Electronics	Indian Institute of technology Delhi, HauzKhas, New Delhi	Distinguished lecture on EMTL	K.S auditorium	17-07-2015

20	Sri. Mahesh U Patel	Principal Technical Officer	C- DAC, Hyderabad	A Developer's Perspective of Becoming an Embedded System Expert	K.S auditorium	06-06-2015
<b>2014-15</b>						
<b>S.No</b>	<b>Name &amp; of the resource person</b>	<b>designation</b>	<b>Organization</b>	<b>Topic of guest lecture</b>	<b>Venue</b>	<b>Date</b>
1	Mr,K. Srinivasulu,	Director	DEPIK Technologies, Hyderabad	Latest Embedded Processors	K.S auditorium	30-07-2014
2	P.Parameswara Rao, M.Ram Kumar,	Manager Engineering operations	Seer AkademiPvt Ltd.	ASIC Design Flow Using Industry Standard EDA Tools	B-block Seminar Hall	21-08-2014
3	Mr. Anil saini	Engineer	L & T Metro rail Hyderabad.	Communications	K.S auditorium	22/ 11/2014
4	Ms. Najma begum	Network Engineer,	Zonta Technologies	Networking: an Over View of ideas and issues	K.S auditorium	15-12-2014

5	Mr. Vikas Reddy Rapuru,	Technical Lead	Intello Tech Solutions, Hyderabad	Embedded system connectivity with LabVIEW	B-block Seminar Hall	09-01- 2015
6	M.Shiva Kumar,	Senior Manager	AMD R&D centre Hyderabad	Timing Analysis	K.S auditorium	23-01- 2015
7	Mr. Mahesh U Patil,	Principal Technical Officer	C-DAC Hyderabad	Mastering Embedded Processors	B-block Seminar Hall	24-01- 2015
8	Dr. Anikieedu Velaga	Senior Vice President	Kulim, Kedah, Malaysia	Motivation	B-block Seminar Hall	10/03/2015
9	Mr. Mahesh U Patil,	Principal Technical Officer	C-DAC Hyderabad	Embedded Systems	B-block Seminar Hall	6/6/2015