

**VNR VIGNANA JYOTHI INSTITUTE OF ENGINEERING & TECHNOLOGY  
(AUTONOMOUS)**



Vignana Jyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad - 500 090

**EXAMINATIONS BRANCH**

**NOTIFICATION FOR Ph.D PROGRAMME (Course Work) UNDER NDF, JANUARY-2020**

**Ph.D PROGRAMME (Course Work) UNDER NDF, REGULAR EXAMINATIONS  
[For R18 Regulations]**

The students appearing for the above Examinations commencing from 22-01-2020 are informed to note the time schedule given below.

<b>ONLINE REGISTRATION</b>	<b>LAST DATE</b>
Without Late Fee	05-01-2020
With Late Fee of Rs. 100/-	08-01-2020
With Late Fee of Rs. 1000/-	11-01-2020
With Late Fee of Rs. 3000/-	15-01-2020

**EXAMINATION FEE**

<b>1. FOR WHOLE EXAMINATION (ALL SUBJECTS)</b>	<b>Rs. 1515/-</b>
--	-------------------

**Note: -**

1. Fee can be paid at student service center, VNRVJIET. The registration forms along with fee receipt should be submitted at the student service center of the institute.
2. The eligibility of the candidates for appearing for the Examination is subject to satisfying the academic & attendance requirements.
3. Hall Tickets to the candidates will be issued only after clearing the dues, if any.
4. Please go through the website for detailed Time-Tables
5. In case of discrepancies in the registration, please contact office of the Controller of Examinations.

**DATE: 28-12-2019**

  
**Controller of Examinations**

28/12/2019.

**Copy to:**

All HOD's, (With a Request to circulate it to the students under their control)

Accounts Section (For Information)

MIS (For Website)

Principal, Dean (Admin., & Fin.), Dy. Dean (Admin., & Fin.), Dean (Academics) for Information

PAAC (For Information)

Sports & Library

CE, EEE, ME, ECE, CSE, EEE, IT, AME, IAS

# VNR VIGNANA JYOTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

VignanaJyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad - 90

## TIMETABLE FOR Ph.D PROGRAMME (Course Work) UNDER NDF, REGULAR EXAMINATION - JANUARY, 2020

22 January 2020 10:00 AM to 1:00 PM	24 January 2020 10:00 AM to 1:00 PM	27 January 2020 10:00 AM to 1:00 PM	29 January 2020 10:00 AM to 1:00 PM	31 January 2020 10:00 AM to 1:00 PM
Wednesday	Friday	Monday	Wednesday	Friday
Power Electronic Converters (18PC1PL01)	--	Electric Drive System (18PC1PL03)	--	Renewable Energy System (18PE1PS01)
--	--	--	Finite Element Analysis (18PC1CD01)	--
--	Digital IC Design (18PC1VS02)	Analog IC design (18PC1VS03)	Internet of Things (18PE1ES02)	--
--	--	--	Machine Learning (18PC1CP03)	--
--	Transducers and Applications (18PC1LJ01)	--	--	Neural Networks & Fuzzy Systems (18PE1LJ04)

*Narant*  
31/12/2019  
Controller of Examinations

Copy to :All HOD's

Note: 1. ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE EXAMINATION BRANCH IMMEDIATELY  
2. EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS PER SCHEDULE.