

Registration Form

For One Week Workshop

On
“Integrated Circuit and System Design using
CAD Tools”
12th - 17th June’ 2017

Name:

Designation:

ISTE and IETE Membership No.:

Organization:

Address

Phone No. :(O) (R)

Mobile No.:

E-mail ID:

Whether the Institute has AICTE approval: Yes / No

Sex:

Accommodation Wanted: Yes/No

Category: Academic/Industry

Signature of the Participant:

Date:

Place:

Signature of the Head of the Institute/
Sponsoring Authority
(With Date and Seal)

About VNRVJIET

“Valluripalli Nageswara Rao (VNR) Vignana Jyothi Institute of Engineering and Technology” was established by “VIGNANA JYOTHI. “Vignana Jyothi” was carved and created in the year 1991 by a dedicated group of Industrialists, Entrepreneurs and Professionals who felt that education is the light that wipes out the darkness of an uncertain future among the youth and were determined to impart quality education to them without profit motive. An Autonomous Institute, approved by AICTE, affiliated to JNTUH was established in the year 1995 and is accredited by NAAC with A-Grade and also 7 UG & 4PG programmes are accredited by NBA. UGC recognized VNR VJIET as College with Potential for Excellence Status. Being one of the most preferred colleges in T.S., VNRVJIET is well known for its discipline, technical excellence, infrastructure, record student performance, research activities, etc.

The institute has established exclusive research facilities in most branches of engineering and science. The Research and consultancy cell (RCC) handles sponsored and consultancy projects in the advanced areas of science and technology.

The VNRVJIET is located in Bachupally, Ranga Reddy District, about 8 km from Miyapur junction along the inner ring road. Also it is about 6 km via Pragathi Nagar, from JNTU College of Engineering, Kukatpally. Participants can avail college transport facility free of cost.

Contact Details

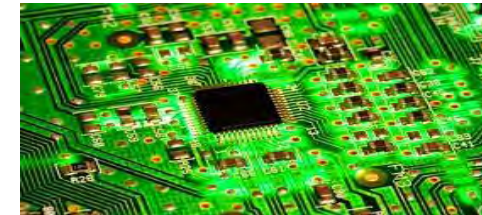
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One week Workshop
On
“Integrated circuit and System
Design using CAD Tools ”
12th -17th June’2017



Organized by

Department of Electronics and Communication
Engineering,



VNR Vignana Jyothi Institute of Engineering &
Technology
An Autonomous Institution
Bachupally, Hyderabad-90

ECE Department

The Department offers B.Tech in ECE and two M.Tech courses in VLSI System Design and Embedded Systems. The Department has very well equipped laboratories catering to the fields of Communications, Microprocessors, Microcontrollers, Image Processing, VLSI System Design including Digital, Analog and Mixed Signal processing and DSP processors. The department has a DSP Laboratory with Advanced DSP Processors, Code Composer Studio Software and the latest Embedded Processors such as OMAP, MSP430 and Microprocessors.

The department has 16 Labs with fully furnished infrastructure. All the laboratories are provided with good infrastructural facilities at a total cost of Rs.2.08 crores. The department has 65 well experienced and qualified teaching faculty and 07 supporting staff. Most of the faculty of the department are working in Research and Consultancy Cell which comprises of RFID and WSN Lab, Virtual Reality Lab, Machine Vision Lab. The ongoing sponsored Research projects are Automated Commando Training System (ACTS) and Mobile Image Position and Performance Acquisition System (MIPPAS). The Department conducts intensive courses, seminars, workshops and technical symposia on latest trends and technologies. The ECE Department has received 60 Lakhs grants from various funding agencies.

About the Workshop

Electronics & communication industry in India is growing at a brisk pace and is extremely competitive. A plethora of new technologies are flooding the Indian markets. In this dynamic scenario, understanding these technologies is possible only through the right blend of academics and practical training. Faculty and students who want to pursue their research and careers in this industry need to be aware of the industry expectations and upgrade themselves on all technologies constantly.

This training will give the participants an invaluable resource for those who are trying to enhance their knowledge about current needs in VLSI industry. This program is aimed and designed to provide a better exposure for faculty in the design and verification of digital and analog circuits using frontend and backend tools. This program aims to provide extensive and complete training with hands on sessions using CAD tools and implementation of various designs. The target audience for this training program may be the faculty and PG students from various engineering colleges.

Course Topics

- Overview of VLSI
- Design through System Verilog
- Verification through System Verilog
- Physical Design flow using Mentor Graphics Tools
- VLSI Design flow using Synopsys Tools
- Prime Time
- Schematic , Layout Design Editor
- DFT Compiler, Library Compiler
- IC Compiler
- Hands on – Frontend ,backend Design using CAD (Synopsys) Tools

Resource Persons

Experts from Nanochip Solutions, Xilinx, CoreEI Technologies, VNRVJIET will conduct the program on current technologies.

Important Dates

Last Date for receiving applications: 9th **June-2017**
Date of Intimation to the Selected Candidates
: 10th **June- 2017**

Registration

Participants from various Engineering colleges have to pay Rs.1000/- per candidate as registration fee, which is to be paid in advance on or before 9th **June-2017**.

Advisory committee

Dr.C.D. Naidu, Principal, VNRVJIET

Organizing committee

Dr. Y.Padma Sai, HOD, ECE
Dr.L.Padma Sree, Professor, ECE
Dr.S.Rajendra Prasad, Professor, ECE

Co-Ordinators

Ms. J.L.V.Ramana Kumari, Asst. Professor, ECE
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Mr. K. Sharath Chandra , Asst. Prof, ECE
Mobile No.7416538641

Accommodation & Transport Committee

Mr. A. Ramesh Kumar, Assoc. Prof, ECE
Mobile No. 984707259
Mr. P.Kishore , Asst. Prof, ECE
Mobile No. 9866940403

Hospitality & Reception Committee

Ms. L.D.Teja Asst. Prof, ECE
Ms.V.Priyanka , Asst. Prof, ECE
Ms.G.Shanthi , Asst. Prof, ECE
Mr.Ch.Ganesh , Asst. Prof, ECE
Mr.K.Naresh , Asst. Prof, ECE
Ms.K.Swetha Reddy, Asst. Prof, ECE