

Registration Form

For TEQIP-II sponsored Workshop On

**“Low Power Embedded Systems using ARM
Cortex-M4F based MSP432”
16th - 18th June 2016**

Name:

Designation:

ISTE and IETE Membership No.:

Organization:

Address

Phone No. :(O) (R)

Mobile No.:

E-mail ID:

Whether the Institute has AICTE approval: Yes / No

Sex:

Accommodation Wanted: Yes/No

Category: Academic/Industry

Signature of the Participant:

Date:

Place:

Signature of the Head of the Institute/
Sponsoring Authority
(With Date and Seal)

About VNRVJIET

“Vallurupalli Nageswara Rao (VNR) Vignana Jyothi Institute of Engineering and Technology” was established by “VIGNANA JYOTHI. “Vignana Jyothi” was carved and created in the year 1991 by a dedicated group of Industrialists, Entrepreneurs and Professionals who felt that education is the light that wipes out the darkness of an uncertain future among the youth and determined to impart quality education to them without profit motive. The institute, approved by AICTE, affiliated to JNTU was established in the year 1995 and is accredited by NBA (both UG &PG programmes of ECE). Being one of the most preferred colleges in T.S, VNRVJIET is well known for its discipline, technical excellence, infrastructure, record student performance, Research activities, etc.

The institute has established exclusive research facilities in most branches of engineering and science. The Research and consultancy cell (RCC) handles sponsored and consultancy projects in the advanced areas of science and technology.

The VNRVJIET is located in Bachupally, Ranga Reddy District, about 8 km from Miyapur junction along the inner ring road. Also it is about 6 km via Pragathi Nagar, from JNTU College of Engineering, Kukatpally. Participants can avail college transport facility free of cost.

Contact Details

Tel: 040-23042758/59/60, Fax: 040-23042761

Web site : www.vnrvjiet.ac.in,

E-mail: ecehead@vnrvjiet.in, padmasai_y@vnrvjiet.in,
rajanikumari_lv@vnrvjiet.in, archana_k@vnrvjiet.in

Mobile Phone No : 9603187807,9000455775

TEQIP sponsored
Workshop
On

**“Low Power
Embedded Systems
using ARM[®] Cortex[®] -
M4F based MSP432”**



Co-Ordinators

Ms.K.B.Archana & Ms.L.V. Rajani Kumari



Organized by

Department of Electronics and Communication
Engineering & ISTE, IETE, VSI Chapters and IIPC
of

VNR Vignana Jyothi Institute of Engineering &
Technology

in collaboration with

M/S Texas Instruments (India) Pvt. Ltd.,
Bangalore

ECE Department

The Department offers B.Tech in ECE and two M.Tech courses in VLSI System Design and Embedded Systems. The Department has very well equipped laboratories catering to the fields of Communications, Microprocessors, Microcontrollers, Image Processing, VLSI System Design including Digital, Analog and Mixed Signal processing and DSP processors. The department has a DSP Laboratory with Advanced DSP Processors, Code Composer Studio Software and the latest Embedded Processors such as OMAP, MSP430 and Microprocessors.

The department has 18 Labs with fully furnished infrastructure. All the laboratories are provided with good infrastructural facilities at a total cost of Rs.3. 8 crores. The department has 50 well experienced and qualified teaching faculty and 09 supporting staff. Most of the faculty of the department is working in Research and Consultancy Cell which comprises of RFID and WSN Lab, Virtual Reality Lab, Machine Vision Lab. The ongoing sponsored Research projects are Automated Commando Training System (ACTS) and Mobile Image Position and Performance Acquisition System (MIPPAS). The Department conducts intensive courses, seminars, workshops and technical symposia on latest trends and technologies. The ECE Department has received 2 crore grants from various funding agencies.

About the Workshop

The aim of this workshop is to provide both theoretical and hands-on experience on Embedded-System using ARM® Cortex®-M4F based MSP432 as well as give awareness about the applications which can be developed using it (such as Wireless Sensor Networks,

Medical Applications, Wearable Electronics, and many more, where power efficiency is critical). The MSP432 is a mixed-signal microcontroller family from Texas Instruments (TI). The MSP432 high-performance, low-power platform has been designed to adopt the industry ARM® Cortex®-M4F standard while staying true to MSP ultra-low-power DNA. It is designed for low cost and, specifically, low power consumption embedded applications. The lab exercises will be using the MSP432 LaunchPad Evaluation Kit and TI Code Composer Studio (CCS).

Course Topics

- MSP432 Overview
- Overview of Cortex M4F
- Brief introduction to ARM Cortex-M
- Power System
- Hands on – Power modes
- Clock System
- Hands on – Clock manipulation
- Memory System
- Hands on – Memory Configuration Bit banding
- Digital Peripherals
- Analog Peripherals
- Hands on – ADC14
- Floating point unit
- Fault handling
- Security (MPU)
- Software
- Debug features

Resource Persons

Experts from M/S Texas Instruments, Bangalore, IITH, & VNRVJIET will conduct the program on current technologies.

Important Dates

Last Date for receiving applications: **6th June-2016**

Date of Intimation to the Selected Candidates

: 13th June-2016

Registration

Participants from various Engineering colleges have to pay Rs. 500/- per candidate as registration fee, which is to be paid in advance on or before **13th June-2016**.

Organizing committee

Prof. C.D.Naidu, Director, PG studies, VNRVJIET
Prof. C.Kiranmai, Principal, VNRVJIET
Prof. Y.Padma Sai, HOD, ECE & Dean Student Progression, VNRVJIET

Coordinators

Ms.K.B.Archana, Senior Asst. Professor, ECE
Ms.L.V. Rajani Kumari, Asst. Professor, ECE
Mobile No. 9603187807

Hospitality & Reception Committee

Mr.A. Ramesh Kumar, Assoc. Prof, ECE
Mrs.N.Dhana Lakshmi, Assoc. Prof, ECE
Mrs. K.S.Shilpa, Asst. Prof, ECE
Mrs. V.Priyanka, Asst. Prof, ECE
Mrs. Ch. Naga Deepa, Asst. Prof, ECE
Mr.V.Naveen Kumar, Asst. Prof, ECE